

Course of Study Microelectronics and Microsystems (Study Cohort w20)

Sample course plan M Master Microelectronics and Microsystems (IMPMM)

Core Qualification Compulsory Specialisation Compulsory Focus Compulsory Thesis Compulsory
 Core Qualification Elective Compulsory Specialisation Elective Compulsory Focus Elective Compulsory Interdisciplinary complement

Specialisation Embedded Systems			
1	Microsystem Engineering		Microsystem Design
2	Microsystem Engineering VL 2		Microsystem Design VL 2
3	Microsystem Engineering PBL 2		Microsystem Design PR 3
4			
5			
6			
7	Microsystems Technology in Theory and Practice		Semiconductor Technology
8	Microsystems Technology VL 2		Semiconductor Technology VL 4
9	Microsystems Technology PBL 2		Semiconductor Technology PR 2
10			
11			
12			
13	Integrated Circuit Design		Advanced IC Design
14	Integrated Circuit Design VL 3		Advanced IC Design VL 2
15	Integrated Circuit Design GÜ 1		Advanced IC Design PBL 2
16			
17			
18			Seminar for IMPMM
19			Seminar for IMPMM SE 2
20		Software for Embedded Systems	Advanced System-on-Chip Design (Lab)
21		Software for Embedded Systems VL 2	Advanced System-on-Chip Design PBL 3
22		Software for Embedded Systems GÜ 3	
23			
24			
25		Design of Dependable Systems	
26		Designing Dependable Systems VL 2	
27		Designing Dependable Systems GÜ 2	
28			
29			
30			
Business & Management (from catalogue) - 6LP			
Non-technical Courses for Master (from catalogue) - 6LP			
Technical Elective Complementary Course for IMPMM - field ET (according to Subject Specific Regulations) - 6LP			

The choice of courses from the catalogue is flexible (depends on the semestral work load), provided the necessary number of required credits is reached.

