## Course of Study Microelectronics and Microsystems (Study Cohort w19) Tress Security Computer Special Compu

Sample	course plan Y Master Microelectronics a	nd Microsyst	ems (IMPMM)			Core qualification Elective Compulsory  Specialisation Compu		Interdisciplinary complement
	isation Microelectronics Complements	Form Hrs/wk	Semester 2	Form Hrs/wk	Semester 3	Form Hrs/wk	Semester 4	Form Hrs/wk
1 2 3	Microsystem Engineering Microsystem Engineering Microsystem Engineering	VL 2 PBL 2	Microsystem Design Microsystem Design Microsystem Design	VL 2 PR 3	Project Work IMPMM		<b>Digital Circuit Design (part 2)</b> Advanced Digital Circuit Design	VL 2
4 5 6							Master Thesis	
7 8 9	Microsystems Technology in Theory and Practice Microsystems Technology Microsystems Technology	VL 2 PBL 2	Fundamentals of IC Design Fundamentals of IC Design Fundamentals of IC Design	VL 2 PR 2				
10 11 12								
13 14 15	CMOS Nanoelectronics with Practice CMOS Nanoelectronics CMOS Nanoelectronics CMOS Nanoelectronics	VL 2 GÜ 1 PR 2	Laboratory: Analog and Digital Circuit Design (part 1) Laboratory: Digital Circuit Design	PR 2				
16 17 18			Semiconductor Seminar Semiconductor Seminar  Semiconductor Technology Semiconductor Technology Semiconductor Technology	SE 2	Laboratory: Analog and Digital Circuit Laboratory: Analog Circuit Design  Digital Circuit Design (part 1) Digital Circuit Design			
19 20 21 22	Electronic Devices and Circuits Circuit Design Electronic Devices	VL 2 VL 2		VL 4 PR 2		<b>1)</b> VL 2		
23 24								
25 26 27 28	Electronic Circuits for Medical Applications	VL 2 GÜ 1 PR 1						
30 31								
33	Business & Management (from catalogue) - 6LP							
	Non-technical Courses for Master (from catalogue) -	6LP						

The choice of courses from the catalogue is flexible (depends on the semestral work load), provided the necessary number of required credits is reached.