Course of Study Microelectronics and Microsystems (Study Cohort w18)

Sample course plan D Master Microelectronics and Microsystems (IMPMM) Specialisation Embedded Systems

Legend:			
Core qualification Compulsory	Specialisation Compulsory	Focus Compulsory	Thesis Compulsory
Core qualification Elective Compulsory	Specialisation Elective Compulsory	Focus Elective Compulsory	Interdisciplinary complement

LP	Semester 1	Form Hrs/w	kSemester 2	Form Hrs/w	kSemester 3 F	orm Hrs/wkS	Semester 4 Form	m Hrs/wk
1 2	Microsystem Engineering Microsystem Engineering	VL 2	Seminar Communications Engineering Seminar Communications Engineering	SE 2	Project Work IMPMM	М	laster Thesis	
3 4 5 6	Microsystem Engineering PBL	PBL 2	Microsystem Design Microsystem Design Microsystem Design	VL 2 PR 3				
7 8 9	Microsystems Technology in Theory and Microsystems Technology Microsystems Technology	Practice VL 2 PBL 2	Fundamentals of IC Design					
11 12 13	ONOS News destruction with Provide		Fundamentals of IC Design Fundamentals of IC Design	VL 2 PR 2				
14 15 16 17	CMOS Nanoelectronics with Practice CMOS Nanoelectronics CMOS Nanoelectronics CMOS Nanoelectronics	VL 2 UE 1 PR 2	Embedded Systems Embedded Systems Embedded Systems	VL 3 UE 1				
19 20 21 22 23 24	Electronic Devices and Circuits Circuit Design Electronic Devices	VL 2 VL 2	Wireless Sensor Networks Wireless Sensor Networks Wireless Sensor Networks	VL 2 UE 1				
25 26 27 28 29	Computer Architecture Computer Architecture Computer Architecture Computer Architecture	VL 2 PBL 2 UE 1	Wireless Sensor Networks: Project	PBL 2				
	Business & Management (from catalogue) - 6 Nontechnical Elective Complementary Course Technical Elective Complementary Course fo	es for Master		ulations) - 6LP				

The choice of courses from the catalogue is flexible (depends on the semestral work load), provided the necessary number of required credits is reached.