Course of Study Microelectronics and Microsystems (Study Cohort w18)

Sample course plan X Master Microelectronics and Microsystems (IMPMM)								Compulsory Focus Elective Compulsory	Interdisciplinary complement
Specia	lisation Communication and Signal Processing	Hrs/wk	Semester 2 Form	Hrs/wk	Semester 3	Form	Hrs/wk	Semester 4	Form Hrs/wk
1 2 3 4 5 6	Microsystem Engineering VL Microsystem Engineering VL Microsystem Engineering PBL	2 2	Seminar Communications Engineering SE Microsystem Design VL Microsystem Design PR	2 2 3	Project Work IMPMM			Master Thesis	
7 8 9 10 11 12	Microsystems Technology in Theory and Practice Microsystems Technology VL Microsystems Technology PBL	2 2	Fundamentals of IC Design VL Fundamentals of IC Design PR	2 2					
13 14 15 16 17	CMOS Nanoelectronics VL CMOS Nanoelectronics VL CMOS Nanoelectronics GÜ CMOS Nanoelectronics PR	2 1 2	Laboratory: Analog and Digital Circuit Design (part 1) Laboratory: Digital Circuit Design PR	2	Laboratory: Analog and Dig				
18 19	Electronic Devices and Circuits		Advanced Concepts of Wireless Communications Advanced Concepts of Wireless Communications VL	3	Laboratory: Analog Circuit Des	ign PR	2		
20 21 22 23 24	Circuit Design VL Electronic Devices VL	2 2	Advanced Concepts of Wireless Communications HÜ	1	Digital Image Analysis Digital Image Analysis	VL	4		
25 26 27 28 29 30	Communication Networks VL Analysis and Structure of Communication Networks VL Communication Networks Excercise PBL Selected Topics of Communication Networks PBL	2 1 2							
	Business & Management (from catalogue) - 6LP								
	Nontechnical Elective Complementary Courses for Master (from catalogue) - 6LP								

esis Compulsory

The choice of courses from the catalogue is flexible (depends on the semestral work load), provided the necessary number of required credits is reached.