## Legend: Sample course plan C Master Microelectronics and Microsystems (IMPMM) Core qualification Compulsory Specialisation Compulsory Thesis Compulsory Focus Compulsory Specialisation Communication and Signal Processing Core qualification Elective Specialisation Elective Focus Elective Compulsory Interdisciplinary complement Compulsory Compulsory ΙP Semester 1 Form Hrs/wk Semester 2 Form Hrs/wk Semester 3 Form Hrs/wk Semester 4 Form Hrs/wk 1 Microsystem Engineering Microsystem Design Project Work IMPMM Master Thesis VL 2 Microsystem Design VL 2 Microsystem Engineering 2 Microsystem Engineering UE Microsystem Design PR 3 3 Microsystem Engineering POL 4 5 6 7 Microsystems Technology in Theory and Practice Fundamentals of IC Design Microsystems Technology VL 2 Fundamentals of IC Design VL 2 8 Microsystems Technology POL 2 Fundamentals of IC Design PR 2 9 10 11 12 13 **CMOS Nanoelectronics with Practice** Laboratory: Analog and Digital Circuit Design (part 1) **CMOS** Nanoelectronics VL 2 Laboratory: Digital Circuit Design PR 2 14 UE **CMOS** Nanoelectronics 15 PR **CMOS** Nanoelectronics 2 16 17 Seminar Communications Engineering Seminar Communications Engineering SE 2 18 19 Electronic Devices and Circuits Laboratory: Analog and Digital Circuit Design (part 2) Circuit Design VL 2 PR 2 Laboratory: Analog Circuit Design 20 VL 2 Electronic Devices 21 22 Digital Image Analysis Digital Image Analysis VL 4 23 24 25 Communication Networks I - Analysis and Structure Analysis and Structure of Communication Networks VL 2 26 POL Communication Networks Excercise 1 27 Selected Topics of Communication Networks POL 2 28 3D Computer Vision 3D Computer Vision VL 2 29 3D Computer Vision UE 2 30 31 32 33 Business & Management (from catalogue) - 6LP

## Course of Study Microelectronics and Microsystems (Study Cohort w16)

The choice of courses from the catalogue is flexible (depends on the semestral work load), provided the necessary number of required credits is reached.