

Course of Study Microelectronics and Microsystems (Study Cohort w14)

Sample course plan A Master Microelectronics and Microsystems (IMPMM)
Specialisation Communication and Signal Processing

Legend:

| | | | |
|-------------------------------|---------------------------|---------------------------|------------------------------|
| Core qualification Compulsory | Specialisation Compulsory | Focus Compulsory | Thesis Compulsory |
| Core qualification Elective | Specialisation Elective | Focus Elective Compulsory | Interdisciplinary complement |
| Compulsory | Compulsory | | |

| LP | Semester 1 | Form | Hrs/wk | Semester 2 | Form | Hrs/wk | Semester 3 | Form | Hrs/wk | Semester 4 | Form | Hrs/wk |
|---|---|------|--------|---|------|--------|---|------|--------|----------------------|------|--------|
| 1 | Microsystem Engineering | | | Microsystem Design | | | Project Work IMPMM | | | Master Thesis | | |
| 2 | Microsystem Engineering | VL | 2 | Microsystem Design | VL | 2 | Project Work IMPMM | PS | 1 | | | |
| 3 | Microsystem Engineering | UE | 1 | Microsystem Design | PR | 3 | | | | | | |
| 4 | | | | | | | | | | | | |
| 5 | | | | | | | | | | | | |
| 6 | | | | | | | | | | | | |
| 7 | Microsystems Technology in Theory and Practice | | | Fundamentals of IC Design | | | | | | | | |
| 8 | Microsystems Technology | VL | 2 | Fundamentals of IC Design | VL | 2 | | | | | | |
| 9 | Microsystems Technology | POL | 2 | Fundamentals of IC Design | PR | 2 | | | | | | |
| 10 | | | | | | | | | | | | |
| 11 | | | | | | | | | | | | |
| 12 | | | | | | | | | | | | |
| 13 | CMOS Nanoelectronics with Practice | | | Laboratory: Analog and Digital Circuit Design (part 1) | | | | | | | | |
| 14 | CMOS Nanoelectronics | VL | 2 | Laboratory: Digital Circuit Design | PR | 2 | | | | | | |
| 15 | CMOS Nanoelectronics | UE | 1 | | | | | | | | | |
| 16 | CMOS Nanoelectronics | PR | 2 | Semiconductor Seminar | | | | | | | | |
| 17 | | | | Semiconductor Seminar | SE | 2 | Laboratory: Analog and Digital Circuit Design (part 2) | | | | | |
| 18 | | | | | | | Laboratory: Analog Circuit Design | PR | 2 | | | |
| 19 | Electronic Devices and Circuits | | | | | | | | | | | |
| 20 | Circuit Design | VL | 2 | | | | Digital Image Analysis | | | | | |
| 21 | Electronic Devices | VL | 2 | | | | Digital Image Analysis | VL | 4 | | | |
| 22 | | | | | | | | | | | | |
| 23 | | | | | | | | | | | | |
| 24 | | | | | | | | | | | | |
| 25 | Microwave Engineering | | | | | | 3D Computer Vision | | | | | |
| 26 | Microwave Engineering | VL | 2 | | | | 3D Computer Vision | VL | 2 | | | |
| 27 | Microwave Engineering | HÜ | 2 | | | | 3D Computer Vision | UE | 2 | | | |
| 28 | Microwave Engineering | PR | 1 | | | | | | | | | |
| 29 | | | | | | | | | | | | |
| 30 | | | | | | | | | | | | |
| 31 | | | | | | | | | | | | |
| Business & Management (from catalogue) - 6LP | | | | | | | | | | | | |
| Nontechnical Elective Complementary Courses for Master (from catalogue) - 6LP | | | | | | | | | | | | |

The choice of courses from the catalogue is flexible (depends on the semestral work load), provided the necessary number of required credits is reached.