

Course of Study Computational Science and Engineering (Study Cohort w18)

Sample course plan E Master Computational Science and Engineering (IIWMS)
Specialisation Information and Communication Technology

Legend:

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|--|------------------------------------|---------------------------|------------------------------|
| Core qualification Compulsory | Specialisation Compulsory | Focus Compulsory | Thesis Compulsory |
| Core qualification Elective Compulsory | Specialisation Elective Compulsory | Focus Elective Compulsory | Interdisciplinary complement |

| LP | Semester 1 | Form Hrs/wk | Semester 2 | Form Hrs/wk | Semester 3 | Form Hrs/wk | Semester 4 | Form Hrs/wk |
|---|--|--------------|---------------------------------------|---------------|---|---------------|---|-------------|
| 1 | Efficient Algorithms | VL 2 UE 2 | Application Security | VL 3 UE 2 | Research Project and Seminar | SE 2 PK 10 | Laboratory: Analog and Digital Circuit Design (part 2) | PR 2 |
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| 6 | | | | | | | | |
| 7 | Software Verification | VL 2 UE 2 | Software for Embedded Systems | VL 2 UE 3 | Master Thesis | | | |
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| 12 | | | | | | | | |
| 13 | Software Security | VL 2 UE 2 | Compilers for Embedded Systems | VL 3 PBL 1 | | | | |
| 14 | | | | | | | | |
| 15 | | | | | | | | |
| 16 | | | Software Testing | VL 2 PBL 2 | | | Advanced System-on-Chip Design (Lab) | PBL 3 |
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| 21 | | | | | | | | |
| 22 | Curves, Codes and Cryptosystems | VL 4 | | | Laboratory: Analog and Digital Circuit Design (part 1) | PR 2 | | |
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| Business & Management (from catalogue) - 6LP | | | | | | | | |
| Nontechnical Elective Complementary Courses for Master (from catalogue) - 6LP | | | | | | | | |

The choice of courses from the catalogue is flexible (depends on the semestral work load), provided the necessary number of required credits is reached.

