Course of Study Computational Science and Engineering (Study Cohort w17)

Sample course plan E Master Computational Science and Engineering (IIWMS)
Specialisation Information and Communication Technology

LP	Semester 1	Form	Hrs/wk	Semester 2	Form	Hrs/wk	Semester 3	Form	Hrs/wk	Semester 4	Form	Hrs/wk
1	**Efficient Algorithms**			**Software for Embedded Systems**			**Research Project and Seminar**			**Laboratory: Analog and Digital Circuit Design (part 2)**		
2												
3	Efficient Algorithms	VL	2	Software for Embdedded Systems	VL	2	Seminar	SE	2	Laboratory: Digital Circuit Design	PR	2
	Efficient Algorithms	UE	2	Software for Embdedded Systems	UE	3	Project Work	PK	10			
4										**Master Thesis**		
5												
6												
7	**Software Verification**			**Compilers for Embedded Systems**								
8												
9	Software Verification	VL	2	Compilers for Embedded Systems	VL	3						
10	Software Verification	UE	2	Compilers for Embedded Systems	FL	1						
11												
12												
13	**Software Security**			**Security in Embedded Hardware**								
14												
15	Software Security	VL	2	Security in Embedded Hardware	VL	2						
16	Software Security	UE	2	Security in Embedded Hardware	UE	2						
17												
18												
19				**Cryptography**			**Advanced System-on-Chip Design (Lab)**					
20												
21				Cryptography	VL	2	Advanced System-on-Chip Design	PBL	3			
22				Cryptography	UE	2						
23												
24												
25				**Codes and Cryptosystems**			**Laboratory: Analog and Digital Circuit Design (part 1)**					
26												
27				Codes and Cryptosystems	VL	4	Laboratory: Analog Circuit Design	PR	2			
28												
29												
30												
31												
32												
33												

| Business & Management (from catalogue) - 6LP |
| Nontechnical Elective Complementary Courses for Master (from catalogue) - 6LP |

The choice of courses from the catalogue is flexible (depends on the semestral work load), provided the necessary number of required credits is reached.