

Course of Study Electrical Engineering (Study Cohort w20)

Sample course plan D Master Electrical Engineering (ETMS)

Core Qualification Compulsory Specialisation Compulsory Focus Compulsory Thesis Compulsory
 Core Qualification Elective Compulsory Specialisation Elective Compulsory Focus Elective Compulsory Interdisciplinary complement

Specialisation Nanoelectronics and Microsystems Technology

1	Digital Communications			Microsystem Design		Research Project and Seminar in Nanoelectronics and Microsystems Technology	Master Thesis
2	Digital Communications	VL	2	Microsystem Design	VL	2	
3	Digital Communications	HÜ	2	Microsystem Design	PR	3	
4	Laboratory Digital Communications	PR	1				
5							
6							
7	Microwave Engineering			Semiconductor Technology			
8	Microwave Engineering	VL	2	Semiconductor Technology	VL	4	
9	Microwave Engineering	HÜ	2	Semiconductor Technology	PR	2	
10	Microwave Engineering	PR	1				
11							
12							
13	Microsystem Engineering			Advanced IC Design		Microsystems Technology in Theory and Practice	
14	Microsystem Engineering	VL	2	Advanced IC Design	VL	2	
15	Microsystem Engineering	PBL	2	Advanced IC Design	PBL	2	
16							
17							
18							
19	Control Systems Theory and Design						
20	Control Systems Theory and Design	VL	2				
21	Control Systems Theory and Design	GÜ	2				
22							
23							
24							
25	Electrical Power Systems II: Operation and Information Systems of Electrical Power Grids						
26	Electrical Power Systems II: Operation and Information Systems of Electrical Power Grids	VL	2				
27	Electrical Power Systems II: Operation and Information Systems of Electrical Power Grids	HÜ	2				
28	Electrical Power Systems II: Operation and Information Systems of Electrical Power Grids						
29							
30							
Business & Management (from catalogue) - 6LP							
Non-technical Courses for Master (from catalogue) - 6LP							
Technical Complementary Course for ETMS (according to Subject Specific Regulations) - 12LP							

The choice of courses from the catalogue is flexible (depends on the semestral work load), provided the necessary number of required credits is reached.

