



## **Module Manual**

Master of Science (M.Sc.)

# **Microelectronics and Microsystems Dual study program**

Cohort: Winter Term 2022

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## Program description

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### Content

Microelectronics, or better named nanoelectronics, because the minimum structure size of state-of-the-art integrated electronic circuits are in the range of 20 nm and below, is the base of the products that significantly influence the daily life of people almost anywhere on earth. Examples are personal computers and smartphones. Both of them open up new possibilities of communication and give access to almost unlimited sources of information, especially when those devices are connected to the world wide web. Another example are medical diagnostic tools for computer tomography or nuclear resonance tomography or intelligent medical implants as all these systems are based on the high computational performance and high data communication efficiency provided by advanced nanoelectronics.

The fundament for microelectronics and microsystems is semiconductor physics and technology. Thus, the objective of the International Master Program "Microelectronics and Microsystems" is to give the students a profound knowledge on physical level about electronic effects in semiconductor materials, especially silicon, and on the functionality of electronic devices. Furthermore, the students are taught about process technology for fabrication of integrated circuits and microsystems. This will enable the students to understand in depth the function of advanced electronic devices and fabrication processes. They will be able to comprehend in a critical way the problems accompanied with the transition to smaller minimum structure sizes. Thus, the students can conceive which possible solutions may exist or could be developed to overcome the problems of scaling-down the device minimum feature size. This will enable the students to understand the ongoing scaling-down of MOS transistors with its potential but also with its limitations.

Besides the essential role of physical basics the precise knowledge of process dependent manufacturing procedures are of key importance for training of the students in the field of nanoelectronics and microsystems. This will help them to develop during their professional life the ability to generate innovative concepts and bring them to practical applications.

The International Master Program "Microelectronics and Microsystems" qualifies the students for scientific professional work in the fields of electrical engineering and information technology. This professional work may extend from the development, production and application to the quality control of complex systems with highly integrated circuits and microsystems components. Both fields are coming closer and closer together, as a fast rising number of complex applications requires the integration of nanoelectronics and microsystems to one combined system.

In particular, this program enables the students not only to design new complex systems for innovative applications, but also to make them usable for practical applications. This can be realized by teaching the students engineering methods both on a physical and theoretical level and on an application oriented level.

In addition to the foundational curriculum taught at TUHH, seminars on developing personal skills are integrated into the dual study programme, in the context of transfer between theory and practice. These seminars correspond to the modern professional requirements expected of an engineer, as well as promoting the link between the two places of learning.

The intensive dual courses at TUHH integrating practical experience consist of an academic-oriented and a practice-oriented element, which are completed at two places of learning. The academic-oriented element comprises study at TUHH. The practice-oriented element is coordinated with the study programme in terms of content and time, and consists of practical modules and phases spent in an affiliate company during periods when there are no lectures.

### Career prospects

The graduates of the International Master Program "Microelectronics and Microsystems" can find a wide variety of professional options as they have well founded knowledge about technology, design and application of highly integrated systems based on nanoelectronics and microsystems.

Thus, one group of possible employers are large companies with international sites for the production of integrated circuits, but also small or medium-sized companies for microsystems. Many job opportunities also exist in the field of development and design of integrated circuits and of microsystems. Because of the fast decline in prices of high-performance computer system, even small companies can conduct tasks that require many computational efforts such as the design of integrated circuits that, then, are fabricated by specialized companies, so-called silicon foundries. This allows many small companies to participate in the market for integrated circuits, so that they can contribute to a good job market for engineers in nanoelectronics and microsystems.

In addition, students acquire basic professional and personal skills as part of the dual study programme that enable them to enter professional practice at an early stage and to go on to further study. Students also gain practical work experience through the integrated practical modules. Graduates of the dual course have broad foundational knowledge, fundamental skills for academic work and relevant personal competences.

### Learning target

#### Knowledge

- The students understand the basic physical principles of microelectronic devices and functional block of microsystems. Furthermore, they have solid knowledge regarding fabrication technologies, so that they can explain them in detail.
- They have gained solid knowledge in selected fields based on a broad theoretical and methodical fundament.
- The students possess in-depth knowledge of interdisciplinary relationships.
- They have the required background knowledge in order to position their professional subjects by appropriate means in the scientific and social environment.

#### Skills

The students are able

- to apply computational methods for quantitative analysis of design parameters and for development of innovative systems for microelectronics and microsystems.
- to solve complex problems and tasks in a self-dependent manner by basic methodical approaches that may be, if necessary, beyond the standard patterns
- to consider technological progress and scientific advancements by taking into account the technical, financial and ecological boundary conditions.

#### Social Skills

The students are capable of

- working in interdisciplinary teams and organizing their tasks in a process oriented manner to become prepared for conducting research based professional work and for taking management responsibilities.
- to present their results in a written or oral form effectively targeting the audience, on international stage also.

# Module Manual M.Sc. "Microelectronics and Microsystems"

## Autonomy

- The students can pervade in an effectively and self-dependently organized way special areas of their professional fields using scientific methods.
- They are able to present their knowledge by appropriate media techniques or to describe it by documents with reasonable lengths.
- The students are able to identify the need for additional information and to develop a strategy for self-dependent enhancement of their knowledge.

By continually switching places of learnings throughout the dual study programme, it is possible for theory and practice to be interlinked. Students reflect theoretically on their individual professional practical experience, and apply the results of their reflection to new forms of practice. They also test theoretical elements of the course in a practical setting, and use their findings as a stimulus for theoretical debate.

## Program structure

The curriculum of the International Master Program „Microelectronics and Microsystems“ is structured as follows:

- Core Qualification:
- Main subject: The students choose one main subject out of the following two options:
- 

The students have to take for their main subjects moduls totaling 18 CPs (1. - 3. semester).

- Master thesis with 30 CP (4. semester)

The sum of required credit points of this Master program is 150 CP.

The structural model of the dual study programme follows a module-differentiating approach. Given the practice-oriented element, the curriculum of the dual study programme is different compared to a standard Bachelor's course. Five practical modules are completed at the dual students' partner company as part of corresponding practical terms during lecture-free periods.

## Core Qualification

### Module M0523: Business & Management

<b>Module Responsible</b>	Prof. Matthias Meyer
<b>Admission Requirements</b>	None
<b>Recommended Previous Knowledge</b>	None
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results
<b>Professional Competence</b> <i>Knowledge</i> <ul style="list-style-type: none"> <li>• Students are able to find their way around selected special areas of management within the scope of business management.</li> <li>• Students are able to explain basic theories, categories, and models in selected special areas of business management.</li> <li>• Students are able to interrelate technical and management knowledge.</li> </ul> <i>Skills</i> <ul style="list-style-type: none"> <li>• Students are able to apply basic methods in selected areas of business management.</li> <li>• Students are able to explain and give reasons for decision proposals on practical issues in areas of business management.</li> </ul> <b>Personal Competence</b> <i>Social Competence</i> <ul style="list-style-type: none"> <li>• Students are able to communicate in small interdisciplinary groups and to jointly develop solutions for complex problems</li> </ul> <i>Autonomy</i> <ul style="list-style-type: none"> <li>• Students are capable of acquiring necessary knowledge independently by means of research and preparation of material.</li> </ul>	
<b>Workload in Hours</b>	Depends on choice of courses
<b>Credit points</b>	6

#### Courses

**Information regarding lectures and courses can be found in the corresponding module handbook published separately.**

Module M0676: Digital Communications				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Digital Communications (L0444)		Lecture	2	3
Digital Communications (L0445)		Recitation Section (large)	2	2
Laboratory Digital Communications (L0646)		Practical Course	1	1
<b>Module Responsible</b>	Prof. Gerhard Bauch			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>• Mathematics 1-3</li> <li>• Signals and Systems</li> <li>• Fundamentals of Communications and Random Processes</li> </ul>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i> The students are able to understand, compare and design modern digital information transmission schemes. They are familiar with the properties of linear and non-linear digital modulation methods. They can describe distortions caused by transmission channels and design and evaluate detectors including channel estimation and equalization. They know the principles of single carrier transmission and multi-carrier transmission as well as the fundamentals of basic multiple access schemes.</p> <p>The students are familiar with the contents of lecture and tutorials. They can explain and apply them to new problems.</p> <p><i>Skills</i> The students are able to design and analyse a digital information transmission scheme including multiple access. They are able to choose a digital modulation scheme taking into account transmission rate, required bandwidth, error probability, and further signal properties. They can design an appropriate detector including channel estimation and equalization taking into account performance and complexity properties of suboptimum solutions. They are able to set parameters of a single carrier or multi carrier transmission scheme and trade the properties of both approaches against each other.</p> <p><b>Personal Competence</b></p> <p><i>Social Competence</i> The students can jointly solve specific problems.</p> <p><i>Autonomy</i> The students are able to acquire relevant information from appropriate literature sources. They can control their level of knowledge during the lecture period by solving tutorial problems, software tools, clicker system.</p>			
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70			
<b>Credit points</b>	6			
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>	<b>Description</b>
	Yes	None	Written elaboration	
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	90 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Core Qualification: Compulsory Computer Science in Engineering: Specialisation II. Engineering Science: Elective Compulsory Information and Communication Systems: Specialisation Communication Systems: Compulsory Information and Communication Systems: Specialisation Secure and Dependable IT Systems, Focus Networks: Elective Compulsory International Management and Engineering: Specialisation II. Information Technology: Elective Compulsory International Management and Engineering: Specialisation II. Electrical Engineering: Elective Compulsory Microelectronics and Microsystems: Core Qualification: Elective Compulsory			

Course L0444: Digital Communications	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Gerhard Bauch
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Repetition: Baseband Transmission                             <ul style="list-style-type: none"> <li>◦ Pulse shaping: Non-return to zero (NRZ) rectangular pulses, raised-cosine pulses, square-root raised-cosine pulses</li> <li>◦ Power spectral density (psd) of baseband signals</li> <li>◦ Intersymbol interference (ISI)</li> <li>◦ First and second Nyquist criterion</li> <li>◦ AWGN channel</li> <li>◦ Matched filter</li> <li>◦ Matched-filter receiver and correlation receiver</li> <li>◦ Noise whitening matched filter</li> <li>◦ Discrete-time AWGN channel model</li> </ul> </li> <li>• Representation of bandpass signals and systems in the equivalent baseband                             <ul style="list-style-type: none"> <li>◦ Quadrature amplitude modulation (QAM)</li> <li>◦ Equivalent baseband signal and system</li> </ul> </li> </ul>

- Analytical signal
- Equivalent baseband random process, equivalent baseband white Gaussian noise process
- Equivalent baseband AWGN channel
- Equivalent baseband channel model with frequency-offset and phase noise
- Equivalent baseband Rayleigh fading and Rice fading channel models
- Equivalent baseband frequency-selective channel model
- Discrete memoryless channels (DMC)
- Bandpass transmission via carrier modulation
  - Amplitude modulation, frequency modulation, phase modulation
  - Linear digital modulation methods
    - On-off keying, M-ary amplitude shift keying (M-ASK), M-ary phase shift keying (M-PSK), M-ary quadrature amplitude modulation (M-QAM), offset-QPSK
    - Signal space representation of transmit signal constellations and signals
    - Energy of linear digital modulated signals, average energy per symbol
    - Power spectral density of linear digital modulated signals
    - Bandwidth efficiency
    - Correlation coefficient of elementary signals
    - Error probabilities of linear digital modulation methods
      - Error functions
      - Gray mapping and natural mapping
      - Bit error probabilities, symbol error probabilities, pairwise symbol error probabilities
      - Euclidean distance and Hamming distance
      - Exact and approximate computation of error probabilities
      - Performance comparison of modulation schemes in terms of per bit SNR vs. per symbol SNR
    - Hierarchical modulation, multilevel modulation
    - Effects of carrier phase offset and carrier frequency offset
    - Differential modulation
      - M-ary differential phase shift keying (M-PSK)
      - Coherent and non-coherent detection of DPSK
      - p/M-differential phase shift keying (p/M-DPSK)
      - Differential amplitude and phase shift keying (DAPSK)
  - Non-linear digital modulation methods
    - Frequency shift keying (FSK)
    - Modulation index
    - Minimum shift keying (MSK)
      - Offset-QPSK representation of MSK
      - MSK with differential precoding and rotation
      - Bit error probabilities of MSK
      - Gaussian minimum shift keying (GMSK)
      - Power spectral density of MSK and GMSK
    - Continuous phase modulation (CPM)
      - General description of CPM signals
      - Frequency pulses and phase pulses
    - Coherent and non-coherent detection of FSK
  - Performance comparison of linear and non-linear digital modulation methods
- Frequency-selective channels, ISI channels
  - Intersymbol interference and frequency-selectivity
  - RMS delay spread
  - Narrowband and broadband channels
  - Equivalent baseband transmission model for frequency-selective channels
  - Receive filter design
- Equalization
  - Symbol-spaced and fractionally-spaced equalizers
  - Inverse system
  - Non-recursive linear equalizers
    - Linear zero-forcing (ZF) equalizer
    - Linear minimum mean squared error (MMSE) equalizer
  - Non-linear equalization:
    - Decision feedback equalizer (DFE)
    - Tomlinson-Harashima precoding
  - Maximum a posteriori probability (MAP) and maximum likelihood equalizer, Viterbi algorithm
- Single-carrier vs. multi-carrier transmission
- Multi-carrier transmission
  - General multicarrier transmission
  - Orthogonal frequency division multiplex (OFDM)
    - OFDM implementation using the Fast Fourier Transform (FFT)
    - Cyclic guard interval
    - Power spectral density of OFDM
    - Peak-to-average power ratio (PAPR)
- Multiple access



	<ul style="list-style-type: none"> <li>◦ Principles of time division multiple access (TDMA), frequency division multiple access (FDMA), code division multiple access (CDMA), non-orthogonal multiple access (NOMA), hybrid multiple access</li> <li>• Spread spectrum communications             <ul style="list-style-type: none"> <li>◦ Direct sequence spread spectrum communications</li> <li>◦ Frequency hopping</li> <li>◦ Protection against eavesdropping</li> <li>◦ Protection against narrowband jammers</li> <li>◦ Short vs. long spreading codes</li> <li>◦ Direct sequence spread spectrum communications in frequency-selective channels                 <ul style="list-style-type: none"> <li>▪ Rake receiver</li> </ul> </li> <li>◦ Code division multiple access (CDMA)                 <ul style="list-style-type: none"> <li>▪ Design criteria of spreading sequences, autocorrelation function and crosscorrelation function of spreading sequences</li> <li>▪ Intersymbol interference (ISI) and multiple access interference (MAI)</li> <li>▪ Pseudo noise (PN) sequences, maximum length sequences (m-sequences), Gold codes, Walsh-Hadamard codes, orthogonal variable spreading factor (OVSF) codes</li> <li>▪ Multicode transmission</li> <li>▪ CDMA in uplink and downlink of a wireless communications system</li> <li>▪ Single-user detection vs. multi-user detection</li> </ul> </li> </ul> </li> </ul>
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<b>Literature</b>	<p>K. Kammeyer: Nachrichtenübertragung, Teubner</p> <p>P.A. Höher: Grundlagen der digitalen Informationsübertragung, Teubner.</p> <p>J.G. Proakis, M. Salehi: Digital Communications. McGraw-Hill.</p> <p>S. Haykin: Communication Systems. Wiley</p> <p>R.G. Gallager: Principles of Digital Communication. Cambridge</p> <p>A. Goldsmith: Wireless Communication. Cambridge.</p> <p>D. Tse, P. Viswanath: Fundamentals of Wireless Communication. Cambridge.</p>
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Course L0445: Digital Communications	
<b>Typ</b>	Recitation Section (large)
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Gerhard Bauch
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Course L0646: Laboratory Digital Communications	
<b>Typ</b>	Practical Course
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Gerhard Bauch
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>- DSL transmission</li> <li>- Random processes</li> <li>- Digital data transmission</li> </ul>
<b>Literature</b>	<p>K. Kammeyer: Nachrichtenübertragung, Teubner</p> <p>P.A. Höher: Grundlagen der digitalen Informationsübertragung, Teubner.</p> <p>J.G. Proakis, M. Salehi: Digital Communications. McGraw-Hill.</p> <p>S. Haykin: Communication Systems. Wiley</p> <p>R.G. Gallager: Principles of Digital Communication. Cambridge</p> <p>A. Goldsmith: Wireless Communication. Cambridge.</p> <p>D. Tse, P. Viswanath: Fundamentals of Wireless Communication. Cambridge.</p>

Module M1048: Integrated Circuit Design				
Courses				
Title	Typ	Hrs/wk	CP	
Integrated Circuit Design (L0691)	Lecture	3	4	
Integrated Circuit Design (L0998)	Recitation Section (small)	1	2	
<b>Module Responsible</b>	Prof. Matthias Kuhl			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Basic knowledge of (solid-state) physics and mathematics. Knowledge in fundamentals of electrical engineering and electrical networks.			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<ul style="list-style-type: none"> <li>• Students can explain basic concepts of electron transport in semiconductor devices (energy bands, generation/recombination, carrier concentrations, drift and diffusion current densities, semiconductor device equations).</li> <li>• Students are able to explain functional principles of pn-diodes, MOS capacitors, and MOSFETs using energy band diagrams.</li> <li>• Students can present and discuss current-voltage relationships and small-signal equivalent circuits of these devices.</li> <li>• Students can explain the physics and current-voltage behavior transistors based on charged carrier flow.</li> <li>• Students are able to explain the basic concepts for static and dynamic logic gates for integrated circuits</li> <li>• Students can exemplify approaches for low power consumption on the device and circuit level</li> <li>• Students can describe the potential and limitations of analytical expression for device and circuit analysis.</li> <li>• Students can explain characterization techniques for MOS devices.</li> </ul>			
<i>Knowledge</i>				
<i>Skills</i>				
<b>Personal Competence</b>				
<i>Social Competence</i>	<ul style="list-style-type: none"> <li>• Students can qualitatively construct energy band diagrams of the devices for varying applied voltages.</li> <li>• Students are able to qualitatively determine electric field, carrier concentrations, and charge flow from energy band diagrams.</li> <li>• Students can understand scientific publications from the field of semiconductor devices.</li> <li>• Students can calculate the dimensions of MOS devices in dependence of the circuits properties</li> <li>• Students can design complex electronic circuits and anticipate possible problems.</li> <li>• Students know procedure for optimization regarding high performance and low power consumption</li> </ul>			
<i>Autonomy</i>	<ul style="list-style-type: none"> <li>• Students can team up with other experts in the field to work out innovative solutions.</li> <li>• Students are able to work by their own or in small groups for solving problems and answer scientific questions.</li> <li>• Students have the ability to critically question the value of their contributions to working groups.</li> </ul>			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56			
<b>Credit points</b>	6			
<b>Course achievement</b>	None			
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	90 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory International Management and Engineering: Specialisation II. Electrical Engineering: Elective Compulsory Mechanical Engineering and Management: Specialisation Mechatronics: Elective Compulsory Mechatronics: Specialisation System Design: Elective Compulsory Microelectronics and Microsystems: Core Qualification: Elective Compulsory			

<b>Course L0691: Integrated Circuit Design</b>	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42
<b>Lecturer</b>	Prof. Matthias Kuhl
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Electron transport in semiconductors</li> <li>• Electronic operating principles of diodes, MOS capacitors, and MOS field-effect transistors</li> <li>• MOS transistor as four terminal device</li> <li>• Performance degradation due to short channel effects</li> <li>• Scaling-down of MOS technology</li> <li>• Digital logic circuits</li> <li>• Basic analog circuits</li> <li>• Operational amplifiers</li> <li>• Bipolar and BiCMOS circuits</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>• Yuan Taur, Tak H. Ning: Fundamentals of Modern VLSI Devices, Cambridge University Press 1998</li> <li>• R. Jacob Baker: CMOS, Circuit Design, Layout and Simulation, IEEE Press, Wiley Interscience, 3rd Edition, 2010</li> <li>• Neil H.E. Weste and David Money Harris, Integrated Circuit Design, Pearson, 4th International Edition, 2013</li> <li>• John E. Ayers, Digital Integrated Circuits: Analysis and Design, CRC Press, 2009</li> <li>• Richard C. Jaeger and Travis N. Blalock: Microelectronic Circuit Design, Mc Graw-Hill, 4rd. Edition, 2010</li> </ul>

<b>Course L0998: Integrated Circuit Design</b>	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 46, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Matthias Kuhl
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M0746: Microsystem Engineering							
Courses							
Title	Typ	Hrs/wk	CP				
Microsystem Engineering (L0680)	Lecture	2	4				
Microsystem Engineering (L0682)	Project-/problem-based Learning	2	2				
<b>Module Responsible</b>	Dr. rer. nat. Thomas Kusserow						
<b>Admission Requirements</b>	None						
<b>Recommended Previous Knowledge</b>	Basic courses in physics, mathematics and electric engineering						
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results						
<b>Professional Competence</b>	<p><i>Knowledge</i> The students know about the most important technologies and materials of MEMS as well as their applications in sensors and actuators.</p> <p><i>Skills</i> Students are able to analyze and describe the functional behaviour of MEMS components and to evaluate the potential of microsystems.</p> <p><b>Personal Competence</b></p> <p><i>Social Competence</i> Students are able to solve specific problems alone or in a group and to present the results accordingly.</p> <p><i>Autonomy</i> Students are able to acquire particular knowledge using specialized literature and to integrate and associate this knowledge with other fields.</p>						
<b>Workload in Hours</b>					Independent Study Time 124, Study Time in Lecture 56		
<b>Credit points</b>					6		
<b>Course achievement</b>					<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>
	No	10 %	Presentation				
<b>Examination</b>	Written exam						
<b>Examination duration and scale</b>	2h						
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Core Qualification: Compulsory International Management and Engineering: Specialisation II. Electrical Engineering: Elective Compulsory International Management and Engineering: Specialisation II. Mechatronics: Elective Compulsory Mechanical Engineering and Management: Specialisation Mechatronics: Elective Compulsory Mechatronics: Specialisation System Design: Elective Compulsory Microelectronics and Microsystems: Core Qualification: Elective Compulsory Theoretical Mechanical Engineering: Specialisation Bio- and Medical Technology: Elective Compulsory						

Course L0680: Microsystem Engineering	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 92, Study Time in Lecture 28
<b>Lecturer</b>	Dr. rer. nat. Thomas Kusserow
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<p>Object and goal of MEMS</p> <p>Scaling Rules</p> <p>Lithography</p> <p>Film deposition</p> <p>Structuring and etching</p> <p>Energy conversion and force generation</p> <p>Electromagnetic Actuators</p> <p>Reluctance motors</p> <p>Piezoelectric actuators, bi-metal-actuator</p> <p>Transducer principles</p> <p>Signal detection and signal processing</p> <p>Mechanical and physical sensors</p> <p>Acceleration sensor, pressure sensor</p> <p>Sensor arrays</p> <p>System integration</p> <p>Yield, test and reliability</p>
<b>Literature</b>	<p>M. Kasper: Mikrosystementwurf, Springer (2000)</p> <p>M. Madou: Fundamentals of Microfabrication, CRC Press (1997)</p>

Course L0682: Microsystem Engineering	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Dr. rer. nat. Thomas Kusserow
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<p>Examples of MEMS components</p> <p>Layout consideration</p> <p>Electric, thermal and mechanical behaviour</p> <p>Design aspects</p>
<b>Literature</b>	Wird in der Veranstaltung bekannt gegeben

Module M1137: Technical Elective Complementary Course for IMPMM - field ET (according to Subject Specific Regulations)			
Courses			
Title	Typ	Hrs/wk	CP
<b>Module Responsible</b>	Prof. Hoc Khiem Trieu		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	Basic knowledge in electrical engineering, physics, semiconductor devices and mathematics at Bachelor of Science level		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b> <i>Knowledge</i>  <i>Skills</i>  <b>Personal Competence</b> <i>Social Competence</i>  <i>Autonomy</i>	<p>As this modul can be chosen from the modul catalogue of the department E, the competence to be acquired is according to the chosen subject.</p> <p>As this modul can be chosen from the modul catalogue of the department E, the skills to be acquired is according to the chosen subject.</p> <ul style="list-style-type: none"> <li>• Students can team up with one or several partners who may have different professional backgrounds</li> <li>• Students are able to work by their own or in small groups for solving problems and answer scientific questions.</li> </ul> <ul style="list-style-type: none"> <li>• Students are able to assess their knowledge in a realistic manner.</li> <li>• The students are able to draw scenarios for estimation of the impact of advanced mobile electronics on the future lifestyle of the society.</li> </ul>		
<b>Workload in Hours</b>	Depends on choice of courses		
<b>Credit points</b>	6		
<b>Assignment for the Following Curricula</b>	Microelectronics and Microsystems: Core Qualification: Elective Compulsory		

Module M0768: Microsystems Technology in Theory and Practice				
Courses				
Title	Typ	Hrs/wk	CP	
Microsystems Technology (L0724)	Lecture	2	4	
Microsystems Technology (L0725)	Project-/problem-based Learning	2	2	
<b>Module Responsible</b>	Prof. Hoc Khiem Trieu			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Basics in physics, chemistry, mechanics and semiconductor technology			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>				
<i>Knowledge</i>	Students are able			
	<ul style="list-style-type: none"> <li>• to present and to explain current fabrication techniques for microstructures and especially methods for the fabrication of microsensors and microactuators, as well as the integration thereof in more complex systems</li> <li>• to explain in details operation principles of microsensors and microactuators and</li> <li>• to discuss the potential and limitation of microsystems in application.</li> </ul>			
<i>Skills</i>	Students are capable			
	<ul style="list-style-type: none"> <li>• to analyze the feasibility of microsystems,</li> <li>• to develop process flows for the fabrication of microstructures and</li> <li>• to apply them.</li> </ul>			
<b>Personal Competence</b>				
<i>Social Competence</i>	Students are able to plan and carry out experiments in groups, as well as present and represent the results in front of others. These social skills are practiced both during the preparation phase, in which the groups work out and present the theory, and during the follow-up phase, in which the groups prepare, document and present their practical experiences.			
<i>Autonomy</i>	The independence of the students is demanded and promoted in that they have to transfer and apply what they have learned to ever new boundary conditions. This requirement is communicated at the beginning of the semester and consistently practiced until the exam. Students are encouraged to work independently by not being given a solution, but by learning to work out the solution step by step by asking specific questions. Students learn to ask questions independently when they are faced with a problem. They learn to independently break down problems into manageable sub-problems.			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56			
<b>Credit points</b>	6			
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>	<b>Description</b>
	Yes	None	Subject theoretical and practical work	andStudierenden führen in Kleingruppen ein Laborpraktikum durch. Jede Gruppe präsentiert und diskutiert die Theorie sowie die Ergebnisse ihrer Labortätigkeit. vor dem gesamten Kurs.
<b>Examination</b>	Oral exam			
<b>Examination duration and scale</b>	30 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Electrical Engineering: Specialisation Medical Technology: Elective Compulsory International Management and Engineering: Specialisation II. Mechatronics: Elective Compulsory Biomedical Engineering: Specialisation Implants and Endoprotheses: Elective Compulsory Biomedical Engineering: Specialisation Medical Technology and Control Theory: Elective Compulsory Biomedical Engineering: Specialisation Management and Business Administration: Elective Compulsory Biomedical Engineering: Specialisation Artificial Organs and Regenerative Medicine: Elective Compulsory Microelectronics and Microsystems: Core Qualification: Elective Compulsory			

Course L0724: Microsystems Technology	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 92, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Hoc Khiem Trieu
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction (historical view, scientific and economic relevance, scaling laws)</li> <li>• Semiconductor Technology Basics, Lithography (wafer fabrication, photolithography, improving resolution, next-generation lithography, nano-imprinting, molecular imprinting)</li> <li>• Deposition Techniques (thermal oxidation, epitaxy, electroplating, PVD techniques: evaporation and sputtering; CVD techniques: APCVD, LPCVD, PECVD and LECVD; screen printing)</li> <li>• Etching and Bulk Micromachining (definitions, wet chemical etching, isotropic etch with HNA, electrochemical etching, anisotropic etching with KOH/TMAH: theory, corner undercutting, measures for compensation and etch-stop techniques; plasma processes, dry etching: back sputtering, plasma etching, RIE, Bosch process, cryo process, XeF2 etching)</li> <li>• Surface Micromachining and alternative Techniques (sacrificial etching, film stress, stiction: theory and counter measures; Origami microstructures, Epi-Poly, porous silicon, SOI, SCREAM process, LIGA, SU8, rapid prototyping)</li> <li>• Thermal and Radiation Sensors (temperature measurement, self-generating sensors: Seebeck effect and thermopile; modulating sensors: thermo resistor, Pt-100, spreading resistance sensor, pn junction, NTC and PTC; thermal anemometer, mass flow sensor, photometry, radiometry, IR sensor: thermopile and bolometer)</li> <li>• Mechanical Sensors (strain based and stress based principle, capacitive readout, piezoresistivity, pressure sensor: piezoresistive, capacitive and fabrication process; accelerometer: piezoresistive, piezoelectric and capacitive; angular rate sensor: operating principle and fabrication process)</li> <li>• Magnetic Sensors (galvanomagnetic sensors: spinning current Hall sensor and magneto-transistor; magnetoresistive sensors: magneto resistance, AMR and GMR, fluxgate magnetometer)</li> <li>• Chemical and Bio Sensors (thermal gas sensors: pellistor and thermal conductivity sensor; metal oxide semiconductor gas sensor, organic semiconductor gas sensor, Lambda probe, MOSFET gas sensor, pH-FET, SAW sensor, principle of biosensor, Clark electrode, enzyme electrode, DNA chip)</li> <li>• Micro Actuators, Microfluidics and TAS (drives: thermal, electrostatic, piezo electric and electromagnetic; light modulators, DMD, adaptive optics, microscanner, microvalves: passive and active, micropumps, valveless micropump, electrokinetic micropumps, micromixer, filter, inkjet printhead, microdispenser, microfluidic switching elements, microreactor, lab-on-a-chip, microanalytics)</li> <li>• MEMS in medical Engineering (wireless energy and data transmission, smart pill, implantable drug delivery system, stimulators: microelectrodes, cochlear and retinal implant; implantable pressure sensors, intelligent osteosynthesis, implant for spinal cord regeneration)</li> <li>• Design, Simulation, Test (development and design flows, bottom-up approach, top-down approach, testability, modelling: multiphysics, FEM and equivalent circuit simulation; reliability test, physics-of-failure, Arrhenius equation, bath-tub relationship)</li> <li>• System Integration (monolithic and hybrid integration, assembly and packaging, dicing, electrical contact: wire bonding, TAB and flip chip bonding; packages, chip-on-board, wafer-level-package, 3D integration, wafer bonding: anodic bonding and silicon fusion bonding; micro electroplating, 3D-MID)</li> </ul>
<b>Literature</b>	<p>M. Madou: Fundamentals of Microfabrication, CRC Press, 2002</p> <p>N. Schwesinger: Lehrbuch Mikrosystemtechnik, Oldenbourg Verlag, 2009</p> <p>T. M. Adams, R. A. Layton: Introductory MEMS, Springer, 2010</p> <p>G. Gerlach; W. Dötzel: Introduction to microsystem technology, Wiley, 2008</p>

Course L0725: Microsystems Technology	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Hoc Khiem Trieu
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course



Module M1759: Linking theory and practice (dual study program, Master's degree)	
<b>Module Responsible</b>	Dr. Henning Haschke
<b>Admission Requirements</b>	None
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>• Successful completion of practical modules as part of the dual Bachelor's course</li> <li>• Module "interlinking theory and practice as part of the dual Master's course"</li> </ul>
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results
<b>Professional Competence</b> <i>Knowledge</i>	Dual students ...  ... can describe and classify selected classic and current theories, concepts and methods <ul style="list-style-type: none"> <li>• related to project management and</li> <li>• change and transformation management</li> </ul> ... and apply them to specific situations, processes and plans in a personal, professional context.
<b>Personal Competence</b> <i>Social Competence</i>	Dual students ... <ul style="list-style-type: none"> <li>• ... can responsibly lead interdisciplinary teams within the framework of complex tasks and problems.</li> <li>• ... engage in sector-specific and cross-sectoral discussions with experts, stakeholders and staff, representing their approaches, points of view and work results.</li> </ul>
<b>Personal Competence</b> <i>Autonomy</i>	Dual students ... <ul style="list-style-type: none"> <li>• ... define, reflect and evaluate goals and measures for complex application-oriented projects and change processes.</li> <li>• ... shape their professional area of responsibility independently and sustainably.</li> <li>• ... take responsibility for their actions and for the results of their work.</li> </ul>
<b>Workload in Hours</b>	Independent Study Time 96, Study Time in Lecture 84
<b>Credit points</b>	6
<b>Course achievement</b>	None
<b>Examination</b>	Written elaboration
<b>Examination duration and scale</b>	Studienbegleitende und semesterübergreifende Dokumentation: Die Leistungspunkte für das Modul werden durch die Anfertigung eines digitalen Lern- und Entwicklungsberichtes (E-Portfolio) erworben. Dabei handelt es sich um eine fortlaufende Dokumentation und Reflexion der Lernerfahrungen und der Kompetenzentwicklung im Bereich der Personalen Kompetenz.

Courses	
<b>Information regarding lectures and courses can be found in the corresponding module handbook published separately.</b>	

<b>Module M1756: Practical module 1 (dual study program, Master's degree)</b>			
<b>Courses</b>			
<b>Title</b>	<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Practical term 1 (dual study program, Master's degree) (L2887)		0	10
<b>Module Responsible</b>	Dr. Henning Haschke		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>Successful completion of a compatible dual B.Sc. at TU Hamburg or comparable practical work experience and competences in the area of interlinking theory and practice</li> <li>Course D from the module on interlinking theory and practice as part of the dual Master's course</li> </ul>		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b>	<p><i>Knowledge</i> Dual students ...</p> <ul style="list-style-type: none"> <li>... combine their knowledge of facts, principles, theories and methods gained from previous study content with acquired practical knowledge - in particular their knowledge of practical professional procedures and approaches, in the current field of activity in engineering.</li> <li>... have a critical understanding of the practical applications of their engineering subject.</li> </ul> <p><i>Skills</i> Dual students ...</p> <ul style="list-style-type: none"> <li>... apply technical theoretical knowledge to complex, interdisciplinary problems within the company, and evaluate the associated work processes and results, taking into account different possible courses of action.</li> <li>... implement the university's application recommendations with regard to their current tasks.</li> <li>... develop solutions as well as procedures and approaches in their field of activity and area of responsibility.</li> </ul>		
<b>Personal Competence</b>	<p><i>Social Competence</i> Dual students ...</p> <ul style="list-style-type: none"> <li>... work responsibly in project teams within their working area and proactively deal with problems within their team.</li> <li>... represent complex engineering viewpoints, facts, problems and solution approaches in discussions with internal and external stakeholders.</li> </ul> <p><i>Autonomy</i> Dual students ...</p> <ul style="list-style-type: none"> <li>... define goals for their own learning and working processes as engineers.</li> <li>... reflect on learning and work processes in their area of responsibility.</li> <li>... reflect on the relevance of subject modules specialisations and specialisation for work as an engineer, and also implement the university's application recommendations and the associated challenges to positively transfer knowledge between theory and practice.</li> </ul>		
<b>Workload in Hours</b>	Independent Study Time 300, Study Time in Lecture 0		
<b>Credit points</b>	10		
<b>Course achievement</b>	None		
<b>Examination</b>	Written elaboration		
<b>Examination duration and scale</b>	Documentation accompanying studies and across semesters: Module credit points are earned by completing a digital learning and development report (e-portfolio). This documents and reflects individual learning experiences and skills development relating to interlinking theory and practice, as well as professional practice. In addition, the partner company provides proof to the dual@TUHH Coordination Office that the dual student has completed the practical phase.		
<b>Assignment for the Following Curricula</b>	Civil Engineering: Core Qualification: Compulsory Bioprocess Engineering: Core Qualification: Compulsory Chemical and Bioprocess Engineering: Core Qualification: Compulsory Computer Science: Core Qualification: Compulsory Electrical Engineering: Core Qualification: Compulsory Energy Systems: Core Qualification: Compulsory Environmental Engineering: Core Qualification: Compulsory Aircraft Systems Engineering: Core Qualification: Compulsory Computer Science in Engineering: Core Qualification: Compulsory Information and Communication Systems: Core Qualification: Compulsory International Management and Engineering: Core Qualification: Compulsory Logistics, Infrastructure and Mobility: Core Qualification: Compulsory Materials Science: Core Qualification: Compulsory Mechanical Engineering and Management: Core Qualification: Compulsory Mechatronics: Core Qualification: Compulsory Biomedical Engineering: Core Qualification: Compulsory Microelectronics and Microsystems: Core Qualification: Compulsory Product Development, Materials and Production: Core Qualification: Compulsory Renewable Energies: Core Qualification: Compulsory Naval Architecture and Ocean Engineering: Core Qualification: Compulsory Theoretical Mechanical Engineering: Core Qualification: Compulsory Process Engineering: Core Qualification: Compulsory		

<b>Course L2887: Practical term 1 (dual study program, Master's degree)</b>	
<b>Typ</b>	
<b>Hrs/wk</b>	0
<b>CP</b>	10
<b>Workload in Hours</b>	Independent Study Time 300, Study Time in Lecture 0
<b>Lecturer</b>	Dr. Henning Haschke
<b>Language</b>	DE
<b>Cycle</b>	WiSe/SoSe
<b>Content</b>	<p><b>Company onboarding process</b></p> <ul style="list-style-type: none"> <li>• Assigning a professional field of activity as an engineer (B.Sc.) and associated fields of work</li> <li>• Establishing responsibilities and authorisation of the dual student within the company as an engineer (B.Sc.)</li> <li>• Working independently in a team and on selected projects - across departments and, if applicable, across companies</li> <li>• Scheduling the current practical module with a clear correlation to work structures</li> <li>• Scheduling the examination phase/subsequent study semester</li> </ul> <p><b>Operational knowledge and skills</b></p> <ul style="list-style-type: none"> <li>• Company-specific: Responsibility as an engineer (B.Sc.) in their own area of work, coordinating team and project work, dealing with complex contexts and unsolved problems, developing and implementing innovative solutions</li> <li>• Subject specialisation (corresponding to the chosen course [M.Sc.]) in the field of activity</li> <li>• Systemic skills</li> <li>• Implementing the university's application recommendations (theory-practice transfer) in corresponding work and task areas across the company</li> </ul> <p><b>Sharing/reflecting on learning</b></p> <ul style="list-style-type: none"> <li>• Creating an e-portfolio</li> <li>• Importance of course contents (M.Sc.) when working as an engineer</li> <li>• Importance of development and innovation when working as an engineer</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>• Studierendenhandbuch</li> <li>• Betriebliche Dokumente</li> <li>• Hochschulseitige Handlungsempfehlungen zum Theorie-Praxis-Transfer</li> </ul>

Module M0747: Microsystem Design				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Microsystem Design (L0683)		Lecture	2	3
Microsystem Design (L0684)		Practical Course	3	3
<b>Module Responsible</b>	Dr. rer. nat. Thomas Kusserow			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Mathematical Calculus, Linear Algebra, Microsystem Engineering			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>				
<i>Knowledge</i>	The students know about the most important and most common simulation and design methods used in microsystem design. The scientific background of finite element methods and the basic theory of these methods are known.			
<i>Skills</i>	Students are able to apply simulation methods and commercial simulators in a goal oriented approach to complex design tasks. Students know to apply the theory in order achieve estimates of expected accuracy and can judge and verify the correctness of results. Students are able to develop a design approach even if only incomplete information about material data or constraints are available. Student can make use of approximate and reduced order models in a preliminary design stage or a system simulation.			
<b>Personal Competence</b>				
<i>Social Competence</i>	Students are able to solve specific problems alone or in a group and to present the results accordingly. Students can develop and explain their solution approach and subdivide the design task to subproblems which are solved separately by group members.			
<i>Autonomy</i>	Students are able to acquire particular knowledge using specialized literature and to integrate and associate this knowledge with other fields.			
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70			
<b>Credit points</b>	6			
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>	<b>Description</b>
	Yes	None	Written elaboration	
<b>Examination</b>	Oral exam			
<b>Examination duration and scale</b>	30 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Microelectronics and Microsystems: Core Qualification: Elective Compulsory			

Course L0683: Microsystem Design	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Dr. rer. nat. Thomas Kusserow
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	Finite difference methods Approximation error Finite element method Order of convergence Error estimation, mesh refinement Makromodeling Reduced order modeling Black-box models System identification Multi-physics systems System simulation Levels of simulation, network simulation Transient problems Non-linear problems Introduction to Comsol Application to thermal, electric, electromagnetic, mechanical and fluidic problems
<b>Literature</b>	M. Kasper: Mikrosystementwurf, Springer (2000) S. Senturia: Microsystem Design, Kluwer (2001)

Course L0684: Microsystem Design	
<b>Typ</b>	Practical Course
<b>Hrs/wk</b>	3
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 48, Study Time in Lecture 42
<b>Lecturer</b>	Dr. rer. nat. Thomas Kusserow
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M0918: Advanced IC Design			
Courses			
Title	Typ	Hrs/wk	CP
Advanced IC Design (L0766)	Lecture	2	3
Advanced IC Design (L1057)	Project-/problem-based Learning	2	3
<b>Module Responsible</b>	Prof. Matthias Kuhl		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	Fundamentals of electrical engineering, electronic devices and circuits		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b> <i>Knowledge</i> <ul style="list-style-type: none"> <li>• Students can explain the basic structure of the circuit simulator SPICE.</li> <li>• Students are able to describe the differences between the MOS transistor models of the circuit simulator SPICE.</li> <li>• Students can discuss the different concept for realization the hardware of electronic circuits.</li> <li>• Students can exemplify the approaches for "Design for Testability".</li> <li>• Students can specify models for calculation of the reliability of electronic circuits.</li> </ul> <i>Skills</i> <ul style="list-style-type: none"> <li>• Students can determine the input parameters for the circuit simulation program SPICE.</li> <li>• Students can select the most appropriate MOS modelling approaches for circuit simulations.</li> <li>• Students can quantify the trade-off of different design styles.</li> <li>• Students can determine the lot sizes and costs for reliability analysis.</li> </ul> <b>Personal Competence</b> <i>Social Competence</i> <ul style="list-style-type: none"> <li>• Students can compile design studies by themselves or together with partners.</li> <li>• Students are able to select the most efficient design methodology for a given task.</li> <li>• Students are able to define the work packages for design teams.</li> </ul> <i>Autonomy</i> <ul style="list-style-type: none"> <li>• Students are able to assess the strengths and weaknesses of their design work in a self-contained manner.</li> <li>• Students can name and bring together all the tools required for total design flow.</li> </ul>			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56		
<b>Credit points</b>	6		
<b>Course achievement</b>	None		
<b>Examination</b>	Written exam		
<b>Examination duration and scale</b>	90 min		
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Microelectronics and Microsystems: Core Qualification: Elective Compulsory		

Course L0766: Advanced IC Design	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Matthias Kuhl
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Circuit-Simulator SPICE</li> <li>• SPICE-Models for MOS transistors</li> <li>• IC design</li> <li>• Technology of MOS circuits</li> <li>• Standard cell design</li> <li>• Design of gate arrays</li> <li>• CMOS transconductance and transimpedance amplifiers</li> <li>• frequency behavior of CMOS circuits</li> <li>• Techniques for improved circuit behaviour (e.g. cascodes, gain boosting, folding, ...)</li> <li>• Examples for realization of ASICs in the institute of nanoelectronics</li> <li>• Reliability of integrated circuits</li> <li>• Testing of integrated circuits</li> </ul>
<b>Literature</b>	<p>R. J. Baker, „CMOS-Circuit Design, Layout, and Simulation“, Wiley &amp; Sons, IEEE Press, 2010</p> <p>B. Razavi, "Design of Analog CMOS Integrated Circuits", McGraw-Hill Education Ltd, 2000</p> <p>X. Liu, VLSI-Design Methodology Demystified; IEEE, 2009</p>

Course L1057: Advanced IC Design	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Matthias Kuhl, Weitere Mitarbeiter
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1131: Technical Elective Complementary Course for IMPMM - field TUHH (according to Subject Specific Regulations)			
Courses			
Title	Typ	Hrs/wk	CP
<b>Module Responsible</b>	Prof. Hoc Khiem Trieu		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	Basic knowledge in electrical engineering, physics, semiconductor devices, software and mathematics at Bachelor of Science level.		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b> <i>Knowledge</i>  <i>Skills</i>  <b>Personal Competence</b> <i>Social Competence</i>  <i>Autonomy</i>	<p>As this module can be chosen from the module catalogue of the TUHH, the competence to be acquired is according to the chosen subject.</p> <p>As this module can be chosen from the module catalogue of the TUHH, the skills to be acquired is according to the chosen subject.</p> <ul style="list-style-type: none"> <li>• Students can team up with one or several partners who may have different professional backgrounds</li> <li>• Students are able to work by their own or in small groups for solving problems and answer scientific questions.</li> </ul>		
<b>Workload in Hours</b>	Depends on choice of courses		
<b>Credit points</b>	6		
<b>Assignment for the Following Curricula</b>	Microelectronics and Microsystems: Core Qualification: Elective Compulsory		



Module M0761: Semiconductor Technology				
Courses				
Title	Typ	Hrs/wk	CP	
Semiconductor Technology (L0722)	Lecture	4	4	
Semiconductor Technology (L0723)	Practical Course	2	2	
<b>Module Responsible</b>	Prof. Hoc Khiem Trieu			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Basics in physics, chemistry, material science and semiconductor devices			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>				
<i>Knowledge</i>	<p>Students are able</p> <ul style="list-style-type: none"> <li>to describe and to explain current fabrication techniques for Si and GaAs substrates,</li> <li>to discuss in details the relevant fabrication processes, process flows and the impact thereof on the fabrication of semiconductor devices and integrated circuits and</li> <li>to present integrated process flows.</li> </ul>			
<i>Skills</i>	<p>Students are capable</p> <ul style="list-style-type: none"> <li>to analyze the impact of process parameters on the processing results,</li> <li>to select and to evaluate processes and</li> <li>to develop process flows for the fabrication of semiconductor devices.</li> </ul>			
<b>Personal Competence</b>				
<i>Social Competence</i>	<p>Students are able to plan and carry out experiments in groups, as well as present and represent the results in front of others. These social skills are practiced both during the preparation phase, in which the groups work out and present the theory, and during the follow-up phase, in which the groups prepare, document and present their practical experiences.</p>			
<i>Autonomy</i>	<p>The independence of the students is demanded and promoted in that they have to transfer and apply what they have learned to ever new boundary conditions. This requirement is communicated at the beginning of the semester and consistently practiced until the exam. Students are encouraged to work independently by not being given a solution, but by learning to work out the solution step by step by asking specific questions. Students learn to ask questions independently when they are faced with a problem. They learn to independently break down problems into manageable sub-problems.</p>			
<b>Workload in Hours</b>	Independent Study Time 96, Study Time in Lecture 84			
<b>Credit points</b>	6			
<b>Course achievement</b>	None			
<b>Examination</b>	Oral exam			
<b>Examination duration and scale</b>	30 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Biomedical Engineering: Specialisation Artificial Organs and Regenerative Medicine: Elective Compulsory Biomedical Engineering: Specialisation Implants and Endoprostheses: Elective Compulsory Biomedical Engineering: Specialisation Medical Technology and Control Theory: Elective Compulsory Biomedical Engineering: Specialisation Management and Business Administration: Elective Compulsory Microelectronics and Microsystems: Core Qualification: Elective Compulsory			

Course L0722: Semiconductor Technology	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	4
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 64, Study Time in Lecture 56
<b>Lecturer</b>	Prof. Hoc Khiem Trieu
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction (historical view and trends in microelectronics)</li> <li>• Basics in material science (semiconductor, crystal, Miller indices, crystallographic defects)</li> <li>• Crystal fabrication (crystal pulling for Si and GaAs: impurities, purification, Czochralski, Bridgeman and float zone process)</li> <li>• Wafer fabrication (process flow, specification, SOI)</li> <li>• Fabrication processes</li> <li>• Doping (energy band diagram, doping, doping by alloying, doping by diffusion: transport processes, doping profile, higher order effects and process technology, ion implantation: theory, implantation profile, channeling, implantation damage, annealing and equipment)</li> <li>• Oxidation (silicon dioxide: structure, electrical properties and oxide charges, thermal oxidation: reactions, kinetics, influences on growth rate, process technology and equipment, anodic oxidation, plasma oxidation, thermal oxidation of GaAs)</li> <li>• Deposition techniques (theory: nucleation, film growth and structure zone model, film growth process, reaction kinetics, temperature dependence and equipment; epitaxy: gas phase, liquid phase, molecular beam epitaxy; CVD techniques: APCVD, LPCVD, deposition of metal silicide, PECVD and LECVD; basics of plasma, equipment, PVD techniques: high vacuum evaporation, sputtering)</li> <li>• Structuring techniques (subtractive methods, photolithography: resist properties, printing techniques: contact, proximity and projection printing, resolution limit, practical issues and equipment, additive methods: liftoff technique and electroplating, improving resolution: excimer laser light source, immersion lithography and phase shift lithography, electron beam lithography, X-ray lithography, EUV lithography, ion beam lithography, wet chemical etching: isotropic and anisotropic, corner undercutting, compensation masks and etch stop techniques; dry etching: plasma enhanced etching, backscattering, ion milling, chemical dry etching, RIE, sidewall passivation)</li> <li>• Process integration (CMOS process, bipolar process)</li> <li>• Assembly and packaging technology (hierarchy of integration, packages, chip-on-board, chip assembly, electrical contact: wire bonding, TAB and flip chip, wafer level package, 3D stacking)</li> </ul>
<b>Literature</b>	<p>S.K. Ghandi: VLSI Fabrication principles - Silicon and Gallium Arsenide, John Wiley &amp; Sons</p> <p>S.M. Sze: Semiconductor Devices - Physics and Technology, John Wiley &amp; Sons</p> <p>U. Hilleringmann: Silizium-Halbleitertechnologie, Teubner Verlag</p> <p>H. Beneking: Halbleitertechnologie - Eine Einführung in die Prozeßtechnik von Silizium und III-V-Verbindungen, Teubner Verlag</p> <p>K. Schade: Mikroelektroniktechnologie, Verlag Technik Berlin</p> <p>S. Campbell: The Science and Engineering of Microelectronic Fabrication, Oxford University Press</p> <p>P. van Zant: Microchip Fabrication - A Practical Guide to Semiconductor Processing, McGraw-Hill</p>

Course L0723: Semiconductor Technology	
<b>Typ</b>	Practical Course
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Hoc Khiem Trieu
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1757: Practical module 2 (dual study program, Master's degree)			
Courses			
Title	Typ	Hrs/wk	CP
Practical term 2 (dual study program, Master's degree) (L2888)		0	10
<b>Module Responsible</b>	Dr. Henning Haschke		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>Successful completion of practical module 1 as part of the dual Master's course</li> <li>course D from the module on interlinking theory and practice as part of the dual Master's course</li> </ul>		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b>	<p><i>Knowledge</i> Dual students ...</p> <ul style="list-style-type: none"> <li>... combine their knowledge of facts, principles, theories and methods gained from previous study content with acquired practical knowledge - in particular their knowledge of practical professional procedures and approaches, in the current field of activity in engineering.</li> <li>... have a critical understanding of the practical applications of their engineering subject.</li> </ul> <p><i>Skills</i> Dual students ...</p> <ul style="list-style-type: none"> <li>... apply technical theoretical knowledge to complex, interdisciplinary problems within the company, and evaluate the associated work processes and results, taking into account different possible courses of action.</li> <li>... implement the university's application recommendations with regard to their current tasks.</li> <li>... develop (new) solutions as well as procedures and approaches in their field of activity and area of responsibility - including in the case of frequently changing requirements (systemic skills).</li> </ul> <p><b>Personal Competence</b></p> <p><i>Social Competence</i> Dual students ...</p> <ul style="list-style-type: none"> <li>... work responsibly in cross-departmental and interdisciplinary project teams and proactively deal with problems within their team.</li> <li>... represent complex engineering viewpoints, facts, problems and solution approaches in discussions with internal and external stakeholders and develop these further together.</li> </ul> <p><i>Autonomy</i> Dual students ...</p> <ul style="list-style-type: none"> <li>... define goals for their own learning and working processes as engineers.</li> <li>... reflect on learning and work processes in their area of responsibility.</li> <li>... reflect on the relevance of subject modules specialisations and specialisation for work as an engineer, and also implement the university's application recommendations and the associated challenges to positively transfer knowledge between theory and practice.</li> </ul>		
<b>Workload in Hours</b>	Independent Study Time 300, Study Time in Lecture 0		
<b>Credit points</b>	10		
<b>Course achievement</b>	None		
<b>Examination</b>	Written elaboration		
<b>Examination duration and scale</b>	Documentation accompanying studies and across semesters: Module credit points are earned by completing a digital learning and development report (e-portfolio). This documents and reflects individual learning experiences and skills development relating to interlinking theory and practice, as well as professional practice. In addition, the partner company provides proof to the dual@TUHH Coordination Office that the dual student has completed the practical phase.		
<b>Assignment for the Following Curricula</b>	Civil Engineering: Core Qualification: Compulsory Bioprocess Engineering: Core Qualification: Compulsory Chemical and Bioprocess Engineering: Core Qualification: Compulsory Computer Science: Core Qualification: Compulsory Electrical Engineering: Core Qualification: Compulsory Energy Systems: Core Qualification: Compulsory Environmental Engineering: Core Qualification: Compulsory Aircraft Systems Engineering: Core Qualification: Compulsory Computer Science in Engineering: Core Qualification: Compulsory Information and Communication Systems: Core Qualification: Compulsory International Management and Engineering: Core Qualification: Compulsory Logistics, Infrastructure and Mobility: Core Qualification: Compulsory Materials Science: Core Qualification: Compulsory Mechanical Engineering and Management: Core Qualification: Compulsory Mechatronics: Core Qualification: Compulsory Biomedical Engineering: Core Qualification: Compulsory Microelectronics and Microsystems: Core Qualification: Compulsory Product Development, Materials and Production: Core Qualification: Compulsory Renewable Energies: Core Qualification: Compulsory Naval Architecture and Ocean Engineering: Core Qualification: Compulsory Theoretical Mechanical Engineering: Core Qualification: Compulsory		

Process Engineering: Core Qualification: Compulsory  
 Water and Environmental Engineering: Core Qualification: Compulsory

Course L2888: Practical term 2 (dual study program, Master's degree)	
<b>Typ</b>	
<b>Hrs/wk</b>	0
<b>CP</b>	10
<b>Workload in Hours</b>	Independent Study Time 300, Study Time in Lecture 0
<b>Lecturer</b>	Dr. Henning Haschke
<b>Language</b>	DE
<b>Cycle</b>	WiSe/SoSe
<b>Content</b>	<p><b>Company onboarding process</b></p> <ul style="list-style-type: none"> <li>• Assigning a professional field of activity as an engineer (B.Sc.) and associated fields of work</li> <li>• Establishing responsibilities and authorisation of the dual student within the company as an engineer (B.Sc.)</li> <li>• Taking personal responsibility within a team and on selected projects - across departments and, if applicable, across companies</li> <li>• Scheduling the current practical module with a clear correlation to work structures</li> <li>• Scheduling the examination phase/subsequent study semester</li> </ul> <p><b>Operational knowledge and skills</b></p> <ul style="list-style-type: none"> <li>• Company-specific: Responsibility as an engineer (B.Sc.) in their own area of work, coordinating team and project work, dealing with complex contexts and unsolved problems, developing and implementing innovative solutions</li> <li>• Subject specialisation (corresponding to the chosen course [M.Sc.]) in the field of activity</li> <li>• Systemic skills</li> <li>• Implementing the university's application recommendations (theory-practice transfer) in corresponding work and task areas across the company</li> </ul> <p><b>Sharing/reflecting on learning</b></p> <ul style="list-style-type: none"> <li>• Updating their e-portfolio</li> <li>• Importance of course contents (M.Sc.) when working as an engineer</li> <li>• Importance of development and innovation when working as an engineer</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>• Studierendenhandbuch</li> <li>• Betriebliche Dokumente</li> <li>• Hochschulseitige Anwendungsempfehlungen zum Theorie-Praxis-Transfer</li> </ul>

Module M1130: Project Work IMPMM			
Courses			
Title	Typ	Hrs/wk	CP
<b>Module Responsible</b>	Dozenten des SD E		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	Good knowledge in the design of electronic circuits, microprocessor systems, systems for signal processing and the handling of software packages for simulation of electrical and physical processes.		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b>			
<i>Knowledge</i>	The student is able to achieve in a specific scientific field special knowledge and she or he can independently acquire in this field the skills necessary for solving these scientific problems.		
<i>Skills</i>	The student is able to formulate the scientific problems to be solved and to work out solutions in an independent manner and to realize them.		
<b>Personal Competence</b>			
<i>Social Competence</i>	The student can integrate herself or himself into small teams of researchers and she or he can discuss proposals for solutions of scientific problems within the team. She or he is able to present the results in a clear and well structured manner.		
<i>Autonomy</i>	The student can perform scientific work in a timely manner and document the results in a detailed and well readable form. She or he is able to anticipate possible problems well in advance and to prepare proposals for their solutions.		
<b>Workload in Hours</b>	Independent Study Time 450, Study Time in Lecture 0		
<b>Credit points</b>	15		
<b>Course achievement</b>	None		
<b>Examination</b>	Study work		
<b>Examination duration and scale</b>	see FSPO		
<b>Assignment for the Following Curricula</b>	Microelectronics and Microsystems: Core Qualification: Compulsory		

Module M1591: Seminar for IMPMM				
Courses				
Title	Typ	Hrs/wk	CP	
Seminar for IMPMM (L2428)	Seminar	2	3	
<b>Module Responsible</b>	Prof. Hoc Khiem Trieu			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Basics from the field of the seminar			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i> Students can explain the most important facts and relationships of a specific topic from the field of the seminar.</p> <p><i>Skills</i> Students are able to compile a specified topic from the field of the seminar and to give a clear, structured and comprehensible presentation of the subject. They can comply with a given duration of the presentation. They can write in English a summary including illustrations that contains the most important results, relationships and explanations of the subject.</p> <p><b>Personal Competence</b></p> <p><i>Social Competence</i> Students are able to adapt their presentation with respect to content, detailedness, and presentation style to the composition and previous knowledge of the audience. They can answer questions from the audience in a curt and precise manner.</p> <p><i>Autonomy</i> Students are able to autonomously carry out a literature research concerning a given topic. They can independently evaluate the material. They can self-reliantly decide which parts of the material should be included in the presentation.</p>			
<b>Workload in Hours</b>				
<b>Credit points</b>				
<b>Course achievement</b>	None			
<b>Examination</b>	Presentation			
<b>Examination duration and scale</b>	15 minutes presentation + 5-10 minutes discussion + 2 pages written abstract			
<b>Assignment for the Following Curricula</b>	Microelectronics and Microsystems: Core Qualification: Compulsory			

Course L2428: Seminar for IMPMM	
<b>Typ</b>	Seminar
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Hoc Khiem Trieu
<b>Language</b>	EN
<b>Cycle</b>	WiSe/SoSe
<b>Content</b>	<p>Prepare, present, and discuss talks about recent topics from the field of semiconductors. The presentations must be given in English.</p> <p><b>Evaluation Criteria:</b></p> <ul style="list-style-type: none"> <li>• understanding of subject, discussion, response to questions</li> <li>• structure and logic of presentation (clarity, precision)</li> <li>• coverage of the topic, selection of subjects presented</li> <li>• linguistic presentation (clarity, comprehensibility)</li> <li>• visual presentation (clarity, comprehensibility)</li> <li>• handout (see below)</li> <li>• compliance with timing requirement.</li> </ul> <p><b>Handout:</b></p> <p>A printed handout (short abstract) of your presentation in English language is mandatory. This should not be longer than two pages A4, and include the most important results, conclusions, explanations and diagrams.</p>
<b>Literature</b>	<p>Aktuelle Veröffentlichungen zu dem gewählten Thema.</p> <p>Recent publications of the selected topics.</p>

Module M1758: Practical module 3 (dual study program, Master's degree)			
Courses			
Title	Typ	Hrs/wk	CP
Practical term 3 (dual study program, Master's degree) (L2889)		0	10
<b>Module Responsible</b>	Dr. Henning Haschke		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>• Successful completion of practical module 2 as part of the dual Master's course</li> <li>• course E from the module on interlinking theory and practice as part of the dual Master's course</li> </ul>		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b>			
<i>Knowledge</i>	Dual students ... <ul style="list-style-type: none"> <li>• ... combine their comprehensive and specialised engineering knowledge acquired from previous study contents with the strategy-oriented practical knowledge gained from their current field of work and area of responsibility.</li> <li>• ... have a critical understanding of the practical applications of their engineering subject, as well as related fields when implementing innovations.</li> </ul>		
<i>Skills</i>	Dual students ... <ul style="list-style-type: none"> <li>• ... apply specialised and conceptual skills to solve complex, sometimes interdisciplinary problems within the company, and evaluate the associated work processes and results, taking into account different possible courses of action.</li> <li>• ... implement the university's application recommendations with regard to their current tasks.</li> <li>• ... develop new solutions as well as procedures and approaches to implement operational projects and assignments - even when facing frequently changing requirements and unpredictable changes (systemic skills).</li> <li>• ... can use academic methods to develop new ideas and procedures for operational problems and issues, and to assess these with regard to their usability.</li> </ul>		
<b>Personal Competence</b>			
<i>Social Competence</i>	Dual students ... <ul style="list-style-type: none"> <li>• ... work responsibly in cross-departmental and interdisciplinary project teams and proactively deal with problems within their team.</li> <li>• ... can promote the professional development of others in a targeted manner.</li> <li>• ... represent complex and interdisciplinary engineering viewpoints, facts, problems and solution approaches in discussions with internal and external stakeholders and develop these further together.</li> </ul>		
<i>Autonomy</i>	Dual students ... <ul style="list-style-type: none"> <li>• ... reflect on learning and work processes in their area of responsibility.</li> <li>• ... define goals for new application-oriented tasks, projects and innovation plans while reflecting on potential effects on the company and the public.</li> <li>• ... reflect on the relevance of areas of specialisation and research for work as an engineer, and also implement the university's application recommendations and the associated challenges to positively transfer knowledge between theory and practice.</li> </ul>		
<b>Workload in Hours</b>	Independent Study Time 300, Study Time in Lecture 0		
<b>Credit points</b>	10		
<b>Course achievement</b>	None		
<b>Examination</b>	Written elaboration		
<b>Examination duration and scale</b>	Documentation accompanying studies and across semesters: Module credit points are earned by completing a digital learning and development report (e-portfolio). This documents and reflects individual learning experiences and skills development relating to interlinking theory and practice, as well as professional practice. In addition, the partner company provides proof to the dual@TUHH Coordination Office that the dual student has completed the practical phase.		
<b>Assignment for the Following Curricula</b>	Civil Engineering: Core Qualification: Compulsory Bioprocess Engineering: Core Qualification: Compulsory Chemical and Bioprocess Engineering: Core Qualification: Compulsory Computer Science: Core Qualification: Compulsory Electrical Engineering: Core Qualification: Compulsory Energy Systems: Core Qualification: Compulsory Environmental Engineering: Core Qualification: Compulsory Aircraft Systems Engineering: Core Qualification: Compulsory Computer Science in Engineering: Core Qualification: Compulsory Information and Communication Systems: Core Qualification: Compulsory International Management and Engineering: Core Qualification: Compulsory Logistics, Infrastructure and Mobility: Core Qualification: Compulsory Materials Science: Core Qualification: Compulsory Mechanical Engineering and Management: Core Qualification: Compulsory Mechatronics: Core Qualification: Compulsory		

Module Manual M.Sc. "Microelectronics and Microsystems"

Biomedical Engineering: Core Qualification: Compulsory  
 Microelectronics and Microsystems: Core Qualification: Compulsory  
 Product Development, Materials and Production: Core Qualification: Compulsory  
 Renewable Energies: Core Qualification: Compulsory  
 Naval Architecture and Ocean Engineering: Core Qualification: Compulsory  
 Theoretical Mechanical Engineering: Core Qualification: Compulsory  
 Process Engineering: Core Qualification: Compulsory  
 Water and Environmental Engineering: Core Qualification: Compulsory

**Course L2889: Practical term 3 (dual study program, Master's degree)**

<b>Typ</b>	
<b>Hrs/wk</b>	0
<b>CP</b>	10
<b>Workload in Hours</b>	Independent Study Time 300, Study Time in Lecture 0
<b>Lecturer</b>	Dr. Henning Haschke
<b>Language</b>	DE
<b>Cycle</b>	WiSe/SoSe
<b>Content</b>	<p><b>Company onboarding process</b></p> <ul style="list-style-type: none"> <li>• Assigning a future professional field of activity as an engineer (M.Sc.) and associated fields of work</li> <li>• Extending responsibilities and authorisation of the dual student within the company up to the intended first assignment after completing their studies</li> <li>• Working responsibly in a team; project responsibility within own area - as well as across divisions and companies if necessary</li> <li>• Scheduling the final practical module with a clear correlation to work structures</li> <li>• Internal agreement on a potential topic or innovation project for the Master's dissertation</li> <li>• Planning the Master's dissertation within the company in cooperation with TU Hamburg</li> <li>• Scheduling the examination phase/subsequent study semester</li> </ul> <p><b>Operational knowledge and skills</b></p> <ul style="list-style-type: none"> <li>• Company-specific: dealing with change, project and team development, responsibility as an engineer in their future field of work (M.Sc.), dealing with complex contexts, frequent and unpredictable changes, developing and implementing innovative solutions</li> <li>• Specialising in one field of work (final dissertation)</li> <li>• Systemic skills</li> <li>• Implementing the university's application recommendations (theory-practice transfer) in corresponding work and task areas across the company</li> </ul> <p><b>Sharing/reflecting on learning</b></p> <ul style="list-style-type: none"> <li>• E-portfolio</li> <li>• Relevance of study content and personal specialisation when working as an engineer</li> <li>• Relevance of research and innovation when working as an engineer</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>• Studierendenhandbuch</li> <li>• betriebliche Dokumente</li> <li>• Hochschuleitige Anwendungsempfehlungen zum Theorie-Praxis-Transfer</li> </ul>



## Specialization Communication and Signal Processing

Students of the specialization Communication and Signal Processing learn both physical and technical basics of state-of-the-art wired and wireless communication systems and the hardware realization of those systems. They can deepen their knowledge towards core areas such as systems for audio or video signal processing. The students understand the fundamental concepts of those systems and can identify their limitations. Based on this knowledge they are able to determine possible improvements and to implement them.

Students have to choose lectures with a total of 18 credit points from the catalog of this specialization.

Module M0836: Communication Networks			
Courses			
Title	Typ	Hrs/wk	CP
Selected Topics of Communication Networks (L0899)	Project-/problem-based Learning	2	2
Communication Networks (L0897)	Lecture	2	2
Communication Networks Exercise (L0898)	Project-/problem-based Learning	1	2
<b>Module Responsible</b>	Prof. Andreas Timm-Giel		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>Fundamental stochastics</li> <li>Basic understanding of computer networks and/or communication technologies is beneficial</li> </ul>		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b>	<p><i>Knowledge</i> Students are able to describe the principles and structures of communication networks in detail. They can explain the formal description methods of communication networks and their protocols. They are able to explain how current and complex communication networks work and describe the current research in these examples.</p> <p><i>Skills</i> Students are able to evaluate the performance of communication networks using the learned methods. They are able to work out problems themselves and apply the learned methods. They can apply what they have learned autonomously on further and new communication networks.</p>		
<b>Personal Competence</b>	<p><i>Social Competence</i> Students are able to define tasks themselves in small teams and solve these problems together using the learned methods. They can present the obtained results. They are able to discuss and critically analyse the solutions.</p> <p><i>Autonomy</i> Students are able to obtain the necessary expert knowledge for understanding the functionality and performance capabilities of new communication networks independently.</p>		
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70		
<b>Credit points</b>	6		
<b>Course achievement</b>	None		
<b>Examination</b>	Presentation		
<b>Examination duration and scale</b>	1.5 hours colloquium with three students, therefore about 30 min per student. Topics of the colloquium are the posters from the previous poster session and the topics of the module.		
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Information and Communication Systems: Elective Compulsory Electrical Engineering: Specialisation Control and Power Systems Engineering: Elective Compulsory Aircraft Systems Engineering: Core Qualification: Elective Compulsory Computer Science in Engineering: Specialisation I. Computer Science: Elective Compulsory Information and Communication Systems: Specialisation Information Systems: Elective Compulsory Information and Communication Systems: Specialisation Secure and Dependable IT Systems, Focus Networks: Elective Compulsory International Management and Engineering: Specialisation II. Information Technology: Elective Compulsory Mechatronics: Technical Complementary Course: Elective Compulsory Microelectronics and Microsystems: Specialisation Communication and Signal Processing: Elective Compulsory Theoretical Mechanical Engineering: Specialisation Robotics and Computer Science: Elective Compulsory		

Course L0899: Selected Topics of Communication Networks	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Andreas Timm-Giel
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	Example networks selected by the students will be researched on in a PBL course by the students in groups and will be presented in a poster session at the end of the term.
<b>Literature</b>	<ul style="list-style-type: none"> <li>see lecture</li> </ul>

Course L0897: Communication Networks	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Andreas Timm-Giel, Dr.-Ing. Koojana Kuladinithi
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	
<b>Literature</b>	<ul style="list-style-type: none"> <li>• Skript des Instituts für Kommunikationsnetze</li> <li>• Tannenbaum, Computernetzwerke, Pearson-Studium</li> </ul> <p>Further literature is announced at the beginning of the lecture.</p>

Course L0898: Communication Networks Exercise	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	1
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 46, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Andreas Timm-Giel
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	Part of the content of the lecture Communication Networks are reflected in computing tasks in groups, others are motivated and addressed in the form of a PBL exercise.
<b>Literature</b>	<ul style="list-style-type: none"> <li>• announced during lecture</li> </ul>

Module M0710: Microwave Engineering				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Microwave Engineering (L0573)		Lecture	2	3
Microwave Engineering (L0574)		Recitation Section (large)	2	2
Microwave Engineering (L0575)		Practical Course	1	1
<b>Module Responsible</b>	Prof. Alexander Kölpin			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Fundamentals of communication engineering, semiconductor devices and circuits. Basics of Wave propagation from transmission line theory and theoretical electrical engineering.			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>				
<i>Knowledge</i>	Students can explain the propagation of electromagnetic waves and related phenomena. They can describe transmission systems and components. They can name different types of antennas and describe the main characteristics of antennas. They can explain noise in linear circuits, compare different circuits using characteristic numbers and select the best one for specific scenarios.			
<i>Skills</i>	Students are able to calculate the propagation of electromagnetic waves. They can analyze complete transmission systems and configure simple receiver circuits. They can calculate the characteristic of simple antennas and arrays based on the geometry. They can calculate the noise of receivers and the signal-to-noise-ratio of transmission systems. They can apply their theoretical knowledge to the practical courses.			
<b>Personal Competence</b>				
<i>Social Competence</i>	Students work together in small groups during the practical courses. Together they document, evaluate and discuss their results.			
<i>Autonomy</i>	Students are able to relate the knowledge gained in the course to contents of previous lectures. With given instructions they can extract data needed to solve specific problems from external sources. They are able to apply their knowledge to the laboratory courses using the given instructions.			
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70			
<b>Credit points</b>	6			
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>	<b>Description</b>
	Yes	None	Subject	theoretical and practical work
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	90 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Core Qualification: Compulsory Information and Communication Systems: Specialisation Communication Systems: Elective Compulsory International Management and Engineering: Specialisation II. Electrical Engineering: Elective Compulsory Microelectronics and Microsystems: Specialisation Communication and Signal Processing: Elective Compulsory			

Course L0573: Microwave Engineering	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Alexander Kölpin
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>- Antennas: Analysis - Characteristics - Realizations</li> <li>- Radio Wave Propagation</li> <li>- Transmitter: Power Generation with Vacuum Tubes and Transistors</li> <li>- Receiver: Preamplifier - Heterodyning - Noise</li> <li>- Selected System Applications</li> </ul>
<b>Literature</b>	<p>H.-G. Unger, „Elektromagnetische Theorie für die Hochfrequenztechnik, Teil I“, Hüthig, Heidelberg, 1988</p> <p>H.-G. Unger, „Hochfrequenztechnik in Funk und Radar“, Teubner, Stuttgart, 1994</p> <p>E. Voges, „Hochfrequenztechnik - Teil II: Leistungsröhren, Antennen und Funkübertragung, Funk- und Radartechnik“, Hüthig, Heidelberg, 1991</p> <p>E. Voges, „Hochfrequenztechnik“, Hüthig, Bonn, 2004</p> <p>C.A. Balanis, „Antenna Theory“, John Wiley and Sons, 1982</p> <p>R. E. Collin, „Foundations for Microwave Engineering“, McGraw-Hill, 1992</p> <p>D. M. Pozar, „Microwave and RF Design of Wireless Systems“, John Wiley and Sons, 2001</p> <p>D. M. Pozar, „Microwave Engineerin“, John Wiley and Sons, 2005</p>

Course L0574: Microwave Engineering	
<b>Typ</b>	Recitation Section (large)
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Alexander Kölpin
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Course L0575: Microwave Engineering	
<b>Typ</b>	Practical Course
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Alexander Kölpin
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M0637: Advanced Concepts of Wireless Communications				
Courses				
Title	Typ	Hrs/wk	CP	
Advanced Concepts of Wireless Communications (L0297)	Lecture	3	4	
Advanced Concepts of Wireless Communications (L0298)	Recitation Section (large)	2	2	
<b>Module Responsible</b>	Dr. Rainer Grünheid			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>Lecture "Signals and Systems"</li> <li>Lecture "Fundamentals of Telecommunications and Stochastic Processes"</li> <li>Lecture "Digital Communications"</li> </ul>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i> Students are able to explain the general as well as advanced principles and techniques that are applied to wireless communications. They understand the properties of wireless channels and the corresponding mathematical description. Furthermore, students are able to explain the physical layer of wireless transmission systems. In this context, they are proficient in the concepts of multicarrier transmission (OFDM), modulation, error control coding, channel estimation and multi-antenna techniques (MIMO). Students can also explain methods of multiple access. On the example of contemporary communication systems (LTE, 5G) they can put the learnt content into a larger context.</p> <p>The students are familiar with the contents of lecture and tutorials. They can explain and apply them to new problems.</p> <p><i>Skills</i> Using the acquired knowledge, students are able to understand the design of current and future wireless systems. Moreover, given certain constraints, they can choose appropriate parameter settings of communication systems. Students are also able to assess the suitability of technical concepts for a given application.</p> <p><b>Personal Competence</b></p> <p><i>Social Competence</i> Students can jointly elaborate tasks in small groups and present their results in an adequate fashion.</p> <p><i>Autonomy</i> Students are able to extract necessary information from given literature sources and put it into the perspective of the lecture. They can continuously check their level of expertise with the help of accompanying measures (such as online tests, clicker questions, exercise tasks) and, based on that, to steer their learning process accordingly. They can relate their acquired knowledge to topics of other lectures, e.g., "Fundamentals of Communications and Stochastic Processes" and "Digital Communications".</p>			
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70			
<b>Credit points</b>	6			
<b>Course achievement</b>	None			
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	90 minutes; scope: content of lecture and exercise			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Information and Communication Systems: Elective Compulsory Information and Communication Systems: Specialisation Communication Systems: Elective Compulsory Microelectronics and Microsystems: Specialisation Communication and Signal Processing: Elective Compulsory			

<b>Course L0297: Advanced Concepts of Wireless Communications</b>	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42
<b>Lecturer</b>	Dr. Rainer Grünheid
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	<p>The lecture deals with technical principles and related concepts of mobile communications. In this context, the main focus is put on the physical and data link layer of the ISO-OSI stack.</p> <p>In the lecture, the transmission medium, i.e., the mobile radio channel, serves as the starting point of all considerations. The characteristics and the mathematical descriptions of the radio channel are discussed in detail. Subsequently, various physical layer aspects of wireless transmission are covered, such as channel coding, modulation/demodulation, channel estimation, synchronization, and equalization. Moreover, the different uses of multiple antennas at the transmitter and receiver, known as MIMO techniques, are described. Besides these physical layer topics, concepts of multiple access schemes in a cellular network are outlined.</p> <p>In order to illustrate the above-mentioned technical solutions, the lecture will also provide a system view, highlighting the basics of some contemporary wireless systems, including LTE, LTE Advanced, and 5G New Radio.</p>
<b>Literature</b>	<p>John G. Proakis, Masoud Salehi: Digital Communications. 5th Edition, Irwin/McGraw Hill, 2007</p> <p>David Tse, Pramod Viswanath: Fundamentals of Wireless Communication. Cambridge, 2005</p> <p>Bernard Sklar: Digital Communications: Fundamentals and Applications. Second Edition, Pearson, 2013</p> <p>Stefani Sesia, Issam Toufik, Matthew Baker: LTE - The UMTS Long Term Evolution. Second Edition, Wiley, 2011</p> <p>Erik Dahlman, Stefan Parkvall, Johan Sköld: 5G NR - The Next Generation Wireless Access Technology. Second Edition, Academic Press, 2021</p>

<b>Course L0298: Advanced Concepts of Wireless Communications</b>	
<b>Typ</b>	Recitation Section (large)
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Dr. Rainer Grünheid
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1700: Satellite Communications and Navigation				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Radio-Based Positioning and Navigation (L2711)		Lecture	2	3
Satellite Communications (L2710)		Lecture	3	3
<b>Module Responsible</b>	Prof. Gerhard Bauch			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	The module is designed for a diverse audience, i.e. students with different background. Basic knowledge of communications engineering and signal processing are of advantage but not required. The course intends to provide the chapters on communications techniques such that on the one hand students with a communications engineering background learn additional concepts and examples (e.g. modulation and coding schemes or signal processing concepts) which have not or in a different way been treated in our other bachelor and master courses. On the other hand, students with other background shall be able to grasp the ideas but may not be able to understand in the same depth. The individual background of the students will be taken into consideration in the oral exam.			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	The students are able to understand, compare and analyse digital satellite communications system as well as navigation techniques. They are familiar with principal ideas of the respective communications, signal processing and positioning methods. They can describe distortions and resulting limitations caused by transmission channels and hardware components. They can describe how fundamental communications and navigation techniques are applied in selected practical systems.			
<i>Knowledge</i>	The students are familiar with the contents of lecture and tutorials. They can explain and apply them to new problems.			
<i>Skills</i>	The students are able to describe and analyse digital satellite communications systems and navigation systems. They are able to analyse transmission chains including link budget calculations. They are able to choose appropriate transmission technologies and system parameters for given scenarios.			
<b>Personal Competence</b>	The students can jointly solve specific problems.			
<i>Social Competence</i>	The students are able to acquire relevant information from appropriate literature sources.			
<i>Autonomy</i>				
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70			
<b>Credit points</b>	6			
<b>Course achievement</b>	None			
<b>Examination</b>	Oral exam			
<b>Examination duration and scale</b>	30 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Information and Communication Systems: Elective Compulsory Information and Communication Systems: Specialisation Secure and Dependable IT Systems, Focus Software and Signal Processing: Elective Compulsory Information and Communication Systems: Specialisation Communication Systems, Focus Signal Processing: Elective Compulsory Microelectronics and Microsystems: Specialisation Communication and Signal Processing: Elective Compulsory			

Course L2711: Radio-Based Positioning and Navigation	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Gerhard Bauch, Dr. Ing. Rico Mendrzik
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Information extraction from communication signals               <ul style="list-style-type: none"> <li>◦ Time-of-arrival principle                   <ul style="list-style-type: none"> <li>▪ Ranging in additive white Gaussian noise (AWGN) channel</li> <li>▪ Correlation-based range estimation</li> <li>▪ Effect of multipath propagation on time-of-arrival principle</li> <li>▪ Zero-forcing range estimation in the presence of multipath</li> <li>▪ Optimum range estimation in the presence of multipath</li> <li>▪ Zero-forcing in presence of noise</li> </ul> </li> <li>◦ Angle-of-arrival principle                   <ul style="list-style-type: none"> <li>▪ Angle-of-arrival estimation in AWGN channel</li> <li>▪ Delay-and-sum estimator</li> <li>▪ Multiple Signal Classifier (MUSIC)</li> </ul> </li> </ul> </li> </ul>

- MUSIC-based angle-of-arrival estimation
- Case study: Comparison of estimators in AWGN channels
- Effect of multipath propagation on angle-of-arrival principle
- Case study: Comparison of estimators in multipath channels
- Information fusion of extracted signals
  - Distance-based positioning
    - Principle of time-of-arrival positioning
    - Geometric interpretation
    - Positioning in the absence of noise
    - Linearization of the positioning problem
    - Positioning in the presence of noise
    - Optimality criteria
    - Least squares time-of-arrival positioning
    - Maximum likelihood time-of-arrival positioning
    - Interactive Matlab demo
    - Excursion: gradient descent solvers for nonlinear programs
    - Real-life positioning with embedded development board (Arduino)
    - Linearized least squares time-of-arrival positioning
    - Effect of clock offsets on distance-based positioning
    - Time-difference-of-arrival principle
    - Least squares time-difference-of-arrival positioning
    - Clock offset mitigation via two-way ranging
  - Performance limits of distance-based positioning
    - Fisher information and the Cramér-Rao lower bound
    - Fisher information in the AWGN case
    - Multi-variate Fisher information
    - Cramér-Rao lower bound for synchronized time-of-arrival positioning
    - Case study: Synchronized time-of-arrival positioning
    - Cramér-Rao lower bound for unsynchronized time-of-arrival positioning
    - Case study: Unsynchronized time-of-arrival positioning
  - Angle-based Positioning
    - Angle-of-arrival positioning principle
    - Geometric interpretation angle-of-arrival positioning principle
    - Noise-free angle-of-arrival positioning with known orientation
    - Effect of noise on angle-of-arrival positioning
    - Least squares angle-of-arrival positioning with known orientation
    - Linear least squares angle-of-arrival positioning
    - Effect of orientation uncertainty
    - Angle-difference-of-arrival positioning
    - Geometric interpretation angle difference of arrival positioning
    - Proof of angle-difference-of-arrival locus
    - Inscribed angle lemma
    - Case study: Angle-difference-of-arrival-positioning
  - Performance limits of angle-based positioning
    - Cramér-Rao lower bound for angle-of-arrival positioning with known orientation
    - Case study: Angle-of-arrival positioning with known orientation
- Information Filtering
  - Bayesian filtering
    - Principle of Bayesian filtering
    - General Problem Formulation
    - Solution to the linear Gaussian case
    - State transition in the linear Gaussian case
    - Proof of predicted posterior distribution of the Kalman filter
    - State update in the linear Gaussian case
    - Proof of marginal posterior distribution of the Kalman filter
    - Working with Gaussian random variables
      - Proof: Affine transformation
      - Proof: Marginalization
      - Proof: Conditioning
    - Kalman filter: Optimum Inference in the linear Gaussian case
    - Modeling of process noise
    - Modeling of measurement noise
    - Case study: Kalman filtering in the linear Gaussian case
    - Interactive Kalman filtering in Matlab
    - Dealing with nonlinearities in Bayesian filtering
    - Nonlinear Gaussian case
    - Extended Kalman filter
    - Proof of predicted posterior distribution of the extended Kalman filter
    - Proof of marginal posterior distribution of the extended Kalman filter
    - Example: Nonlinear state transition



	<ul style="list-style-type: none"> <li>▪ Case study: Extended Kalman filtering</li> <li>▪ Practical considerations for filter design</li> </ul> <ul style="list-style-type: none"> <li>• Satellite Navigation             <ul style="list-style-type: none"> <li>◦ Overview from positioning perspective                 <ul style="list-style-type: none"> <li>▪ Earth-centered earth-fixed (ECEF) coordinate system</li> <li>▪ World geodetic system (WGS)</li> <li>▪ Satellite navigation systems</li> <li>▪ System-receiver clock offsets and pseudo-ranges</li> <li>▪ Unsynchronized time-of-arrival positioning revisited</li> </ul> </li> <li>◦ GPS legacy signals and ranging                 <ul style="list-style-type: none"> <li>▪ Signal overview</li> <li>▪ Time-of-arrival principle revisited</li> <li>▪ Direct sequence spread spectrum principle</li> <li>▪ Short and long codes</li> <li>▪ Satellite signal generation</li> <li>▪ Carriers and codes</li> <li>▪ Correlation properties of codes</li> <li>▪ Code division multiple access in flat fading channels</li> <li>▪ Navigation message</li> </ul> </li> <li>◦ Velocity estimation</li> <li>◦ Hands-on case study: Design of an extended Kalman filter for satellite navigation based on recorded data</li> </ul> </li> <li>• Robust navigation             <ul style="list-style-type: none"> <li>◦ Multipath-assisted positioning in millimeter wave multiple antenna systems</li> <li>◦ Multi-sensor fusion</li> </ul> </li> </ul>
<b>Literature</b>	

Course L2710: Satellite Communications	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 48, Study Time in Lecture 42
<b>Lecturer</b>	Prof. Gerhard Bauch
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction to satellite communications             <ul style="list-style-type: none"> <li>◦ What is a satellite</li> <li>◦ Overview orbits, Van Allen Belt, components of a satellite</li> <li>◦ Satellite services</li> <li>◦ Frequency bands for satellite services</li> <li>◦ International Telecommunications Union (ITU)</li> <li>◦ Influence of atmospheric impairments</li> <li>◦ Milestones in satellite communications</li> </ul> </li> <li>• Components of a satellite communications system             <ul style="list-style-type: none"> <li>◦ Ground segment</li> <li>◦ Space segment</li> <li>◦ Control segment</li> </ul> </li> <li>• Communication links             <ul style="list-style-type: none"> <li>◦ Uplink, downlink</li> <li>◦ Forward link, reverse link</li> <li>◦ Intersatellite links</li> <li>◦ Multiple access</li> <li>◦ Performance measures                 <ul style="list-style-type: none"> <li>▪ Effective isotropic radiated power (EIRP), antenna gain, figure of merit, G/T, carrier to noise ratio</li> <li>▪ Signal to noise power ratio vs. carrier to noise ratio</li> </ul> </li> </ul> </li> <li>• Single beam and multibeam satellites             <ul style="list-style-type: none"> <li>◦ Beam coverage</li> <li>◦ Examples for beam coverage of LEO and GEO satellites (Iridium, Viasat)</li> </ul> </li> <li>• Transparent vs. regenerative payload</li> <li>• Orbits             <ul style="list-style-type: none"> <li>◦ Low earth orbit (LEO), medium earth orbit (MEO), geosynchronous and geostationary orbits (GEO), highly elliptical orbits (HEO)</li> <li>◦ Favourable orbits:                 <ul style="list-style-type: none"> <li>▪ HEO orbits with 63-64° inclination, Molnya and Tundra orbits</li> <li>▪ Circular LEO orbits</li> <li>▪ Circular MEO Orbits (Intermediate Circular Orbits (ICO))</li> <li>▪ Equatorial orbits, geostationary orbit (GEO)</li> </ul> </li> <li>◦ Important aspects of LEO, MEO and GEO satellites</li> </ul> </li> </ul>

- Kepler's laws of planetary motion
- Gravitational force
- Parameters of ellipses and elliptical orbits
  - Major and minor half axis
  - Foci
  - Eccentricity
  - Eccentric anomaly, mean anomaly, true anomaly
  - Area
  - Orbit period
  - Perigee, apogee
  - Distance of satellite from center of earth
  - Construction of ellipses according to de La Hire
  - Orbital plane in space, inclination, right ascension (longitude) of ascending node, Vernal equinox
- Newton's laws of motion
- Newton's universal law of gravitation
  
- Energy of satellites: Potential energy, kinetic energy, total energy
- Instantaneous speed of a satellite
- Kepler's equation
- Satellite visibility, elevation
- Required number of LEO, MEO or GEO satellites for continuous earth coverage
- Satellite altitude and distance from a point on earth
  
- Choice of orbits
  - LEO, HEO, GEO
  - Elliptical orbits with non-zero inclination, Molnya orbits, Tundra orbits
  - Geosynchronous orbits
    - Parameters of geosynchronous orbits
    - Circular geosynchronous orbits
    - Inclined geosynchronous orbits
    - Quasi-zenith satellite systems (QZSS)
    - Syb-synchronous circular equatorial orbits
    - Geostationary orbit
      - Parameters of the geostationary orbit
      - Visibility
      - Propagation delay
      - Applications and system examples
- Perturbations of orbits
  - Station keeping
    - Station keeping box
    - Estimation of orbit parameters
- Fundamentals of digital communications techniques
  - Components of a digital communications system
  - Principles of encryption
  - Scrambling
  - Scrambling vs. interleaving for randomization of data sequences
  - Interleaving: Block interleaver, convolutional interleaver, random interleaver
  - Digital modulation methods
    - Linear and non-linear digital modulation methods
    - Linear digital modulation methods
      - QAM modulator and demodulator
      - Pulse shaping, square-root raised-cosine pulses
      - Average power spectral density
      - Signal space constellation
      - Examples: M-ary phase shift keying (M-PSK), M-ary quadrature amplitude shift keying (M-QAM)
      - M-PSK in noisy channels
      - Bit error probabilities of M-PSK and M-QAM
      - M-PSK vs. M-QAM
      - M-ary amplitude and phase shift keying (M-APSK)
      - M-APSK vs. M-QAM
      - Differential phase shift keying (DPSK)

#### Error control coding (channel coding)

- Error detecting and forward error correcting (FEC) codes
- Principle of channel coding
- Data rate, code rate, Baud rate, spectral efficiency of modulation and coding schemes
- Bandwidth-power trade-off, bandwidth-limited vs. power-limited transmission
- Coding and modulation for transparent vs. regenerative payload
- Block codes and convolutional codes
- Concatenated codes

- Bit-interleaved coded modulation
- Convolutional codes
- Low density parity check (LDPC) codes, principle of message passing decoding, bit error rate performance
- Cyclic block codes
  - Examples for cyclic block codes
  - Single errors vs. block errors, cyclic block codes for burst errors
  - Generator matrix, generator polynomials
  - Systematic encoding and syndrome determination with shift registers
  - Cyclic redundancy check (CRC) codes
  
- Automatic repeat request (ARQ)
  - Principle of ARQ
  - Stop-and-wait ARQ
  - Go-back-N ARQ
  - Selective-repeat ARQ
- Transmission gains and losses
  - Antenna gain
    - Antenna radiation pattern
    - Maximum antenna gain, 3dB beamwidth
    - Maximum antenna gain of circular aperture
    - Maximum antenna gain of a geostationary satellite with global coverage
  - Effective isotropic radiated power (EIRP)
  - Power flux density
  - Path loss
    - Free space loss, free space loss for geostationary satellites
    - Atmospheric loss
    - Received power
  - Losses in transmit and receive equipment
    - Feeder loss
    - Depointing loss
    - Polarization mismatch loss
  - Combined effect of losses
- Noise
  - Origins of noise
  - White noise
  - Noise power spectral density and noise power
  - Additive white Gaussian noise (AWGN) channel model
  - Antenna noise temperature
  - Earth brightness temperature
  - Signal to noise ratios
- Atmospheric distortions
  - Atmosphere of the earth: Troposphere, stratosphere, mesosphere, thermosphere, exosphere
  - Attenuation and depolarization due to rain, fog, rain and ice clouds, sandstorms
  - Scintillation
  - Faraday effect
  - Multipath contributions
- Link budget calculations
  - GEO clear sky uplink and downlink
  - GEO uplink and downlink under rain conditions
  - Transparent vs. regenerative payload
- Link availability improvement through site diversity and adaptive transmission
  - Transparent vs. regenerative payload
    - Non-linear amplifiers
      - Saleh model, Rapp model
      - Input and output back-off factor
    - Single carrier and multicarrier operation
    - Dimensioning of transmission parameters
    - Sources of noise: Thermal noise, interference, intermodulation products
    - Signal to noise ratio and bit error probability
    - Robustness against interference and non-linear channels
- Satellite networks
  - Satellite network reference architectures
  - Network topologies
  - Network connectivity
    - Types of network connectivity
    - On-board connectivity
    - Inter-satellite links
  - Broadcast networks
  - Satellite-based internet

	<ul style="list-style-type: none"><li>• Satellite communications systems and standards examples<ul style="list-style-type: none"><li>◦ The role of standards in satellite communications</li><li>◦ The Digital Video Broadcast Satellite Standard: DVB-S, DVB-S2, DVB-S2X</li><li>◦ Satellites in 3GPP mobile communications networks</li><li>◦ LEO megaconstellations: SpaceX Starlink, Kuiper, OneWeb</li><li>◦ Space debris</li><li>◦ The German Heinrich Hertz mission</li></ul></li></ul>
<b>Literature</b>	

Module M0738: Digital Audio Signal Processing				
Courses				
Title	Typ	Hrs/wk	CP	
Digital Audio Signal Processing (L0650)	Lecture	3	4	
Digital Audio Signal Processing (L0651)	Recitation Section (large)	1	2	
<b>Module Responsible</b>	Prof. Udo Zölzer			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Signals and Systems			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i> Die Studierenden können die grundlegenden Verfahren und Methoden der digitalen Audiosignalverarbeitung erklären. Sie können die wesentlichen physikalischen Effekte bei der Sprach- und Audiosignalverarbeitung erläutern und in Kategorien einordnen. Sie können einen Überblick der numerischen Methoden und messtechnischen Charakterisierung von Algorithmen zur Audiosignalverarbeitung geben. Sie können die erarbeiteten Algorithmen auf weitere Anwendungen im Bereich der Informationstechnik und Informatik abstrahieren.</p> <p><i>Skills</i> The students will be able to apply methods and techniques from audio signal processing in the fields of mobile and internet communication. They can rely on elementary algorithms of audio signal processing in form of Matlab code and interactive JAVA applets. They can study parameter modifications and evaluate the influence on human perception and technical applications in a variety of applications beyond audio signal processing. Students can perform measurements in time and frequency domain in order to give objective and subjective quality measures with respect to the methods and applications.</p> <p><i>Personal Competence</i></p> <p><i>Social Competence</i> The students can work in small groups to study special tasks and problems and will be enforced to present their results with adequate methods during the exercise.</p> <p><i>Autonomy</i> The students will be able to retrieve information out of the relevant literature in the field and put them into the context of the lecture. They can relate their gathered knowledge and relate them to other lectures (signals and systems, digital communication systems, image and video processing, and pattern recognition). They will be prepared to understand and communicate problems and effects in the field audio signal processing.</p>			
<b>Workload in Hours</b>				
<b>Credit points</b>				
<b>Course achievement</b>				
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	60 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Information and Communication Systems: Elective Compulsory Information and Communication Systems: Specialisation Communication Systems, Focus Signal Processing: Elective Compulsory Information and Communication Systems: Specialisation Secure and Dependable IT Systems, Focus Software and Signal Processing: Elective Compulsory Microelectronics and Microsystems: Specialisation Communication and Signal Processing: Elective Compulsory			

Course L0650: Digital Audio Signal Processing	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42
<b>Lecturer</b>	Prof. Udo Zölzer
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction (Studio Technology, Digital Transmission Systems, Storage Media, Audio Components at Home)</li> <li>• Quantization (Signal Quantization, Dither, Noise Shaping, Number Representation)</li> <li>• AD/DA Conversion (Methods, AD Converters, DA Converters, Audio Processing Systems, Digital Signal Processors, Digital Audio Interfaces, Single-Processor Systems, Multiprocessor Systems)</li> <li>• Equalizers (Recursive Audio Filters, Nonrecursive Audio Filters, Multi-Complementary Filter Bank)</li> <li>• Room Simulation (Early Reflections, Subsequent Reverberation, Approximation of Room Impulse Responses)</li> <li>• Dynamic Range Control (Static Curve, Dynamic Behavior, Implementation, Realization Aspects)</li> <li>• Sampling Rate Conversion (Synchronous Conversion, Asynchronous Conversion, Interpolation Methods)</li> <li>• Data Compression (Lossless Data Compression, Lossy Data Compression, Psychoacoustics, ISO-MPEG1 Audio Coding)</li> </ul>
<b>Literature</b>	<p>- U. Zölzer, Digitale Audiosignalverarbeitung, 3. Aufl., B.G. Teubner, 2005 .</p> <p>- U. Zölzer, Digitale Audio Signal Processing, 2nd Edition, J. Wiley &amp; Sons, 2005.</p> <p>- U. Zölzer (Ed), Digital Audio Effects, 2nd Edition, J. Wiley &amp; Sons, 2011.</p>

Course L0651: Digital Audio Signal Processing	
<b>Typ</b>	Recitation Section (large)
<b>Hrs/wk</b>	1
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 46, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Udo Zölzer
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1686: Selected Aspects of Communication and Signal Processing			
<b>Courses</b>			
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b> <b>CP</b>
Selected Aspects of Communication and Signal Processing (L2674)		Lecture	3                  4
Selected Aspects of Communication and Signal Processing (L2675)		Recitation Section (small)	1                  2
<b>Module Responsible</b>	Prof. Hoc Khiem Trieu		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b> <i>Knowledge</i> <i>Skills</i>			
<b>Personal Competence</b> <i>Social Competence</i> <i>Autonomy</i>			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56		
<b>Credit points</b>	6		
<b>Course achievement</b>	None		
<b>Examination</b>	Oral exam		
<b>Examination duration and scale</b>	30 min		
<b>Assignment for the Following Curricula</b>	Microelectronics and Microsystems: Specialisation Communication and Signal Processing: Elective Compulsory		

Course L2674: Selected Aspects of Communication and Signal Processing	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42
<b>Lecturer</b>	Dozenten des SD E
<b>Language</b>	EN
<b>Cycle</b>	WiSe/SoSe
<b>Content</b>	
<b>Literature</b>	

Course L2675: Selected Aspects of Communication and Signal Processing	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 46, Study Time in Lecture 14
<b>Lecturer</b>	Dozenten des SD E
<b>Language</b>	EN
<b>Cycle</b>	WiSe/SoSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1598: Image Processing				
Courses				
Title	Typ	Hrs/wk	CP	
Image Processing (L2443)	Lecture	2	4	
Image Processing (L2444)	Recitation Section (small)	2	2	
<b>Module Responsible</b>	Prof. Tobias Knopp			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Signal and Systems			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>				
<i>Knowledge</i>	The students know about <ul style="list-style-type: none"> <li>• visual perception</li> <li>• multidimensional signal processing</li> <li>• sampling and sampling theorem</li> <li>• filtering</li> <li>• image enhancement</li> <li>• edge detection</li> <li>• multi-resolution procedures: Gauss and Laplace pyramid, wavelets</li> <li>• image compression</li> <li>• image segmentation</li> <li>• morphological image processing</li> </ul>			
<i>Skills</i>	The students can <ul style="list-style-type: none"> <li>• analyze, process, and improve multidimensional image data</li> <li>• implement simple compression algorithms</li> <li>• design custom filters for specific applications</li> </ul>			
<b>Personal Competence</b>				
<i>Social Competence</i>	Students can work on complex problems both independently and in teams. They can exchange ideas with each other and use their individual strengths to solve the problem.			
<i>Autonomy</i>	Students are able to independently investigate a complex problem and assess which competencies are required to solve it.			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56			
<b>Credit points</b>	6			
<b>Course achievement</b>	None			
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	90 min			
<b>Assignment for the Following Curricula</b>	Data Science: Core Qualification: Elective Compulsory Data Science: Specialisation I. Mathematics/Computer Science: Elective Compulsory Electrical Engineering: Specialisation Information and Communication Systems: Elective Compulsory Electrical Engineering: Specialisation Medical Technology: Elective Compulsory Information and Communication Systems: Specialisation Secure and Dependable IT Systems, Focus Software and Signal Processing: Elective Compulsory Information and Communication Systems: Specialisation Communication Systems, Focus Signal Processing: Elective Compulsory International Management and Engineering: Specialisation II. Information Technology: Elective Compulsory Mechatronics: Specialisation Intelligent Systems and Robotics: Elective Compulsory Mechatronics: Specialisation System Design: Elective Compulsory Microelectronics and Microsystems: Specialisation Communication and Signal Processing: Elective Compulsory Theoretical Mechanical Engineering: Specialisation Robotics and Computer Science: Elective Compulsory			



Course L2443: Image Processing	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 92, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Tobias Knopp
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Visual perception</li> <li>• Multidimensional signal processing</li> <li>• Sampling and sampling theorem</li> <li>• Filtering</li> <li>• Image enhancement</li> <li>• Edge detection</li> <li>• Multi-resolution procedures: Gauss and Laplace pyramid, wavelets</li> <li>• Image Compression</li> <li>• Segmentation</li> <li>• Morphological image processing</li> </ul>
<b>Literature</b>	Bredies/Lorenz, Mathematische Bildverarbeitung, Vieweg, 2011 Pratt, Digital Image Processing, Wiley, 2001 Bernd Jähne: Digitale Bildverarbeitung - Springer, Berlin 2005

Course L2444: Image Processing	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Tobias Knopp
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M0677: Digital Signal Processing and Digital Filters				
Courses				
Title	Typ	Hrs/wk	CP	
Digital Signal Processing and Digital Filters (L0446)	Lecture	3	4	
Digital Signal Processing and Digital Filters (L0447)	Recitation Section (large)	2	2	
<b>Module Responsible</b>	Prof. Gerhard Bauch			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>• Mathematics 1-3</li> <li>• Signals and Systems</li> <li>• Fundamentals of signal and system theory as well as random processes.</li> <li>• Fundamentals of spectral transforms (Fourier series, Fourier transform, Laplace transform)</li> </ul>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i> The students know and understand basic algorithms of digital signal processing. They are familiar with the spectral transforms of discrete-time signals and are able to describe and analyse signals and systems in time and image domain. They know basic structures of digital filters and can identify and assess important properties including stability. They are aware of the effects caused by quantization of filter coefficients and signals. They are familiar with the basics of adaptive filters. They can perform traditional and parametric methods of spectrum estimation, also taking a limited observation window into account.</p> <p>The students are familiar with the contents of lecture and tutorials. They can explain and apply them to new problems.</p> <p><i>Skills</i> The students are able to apply methods of digital signal processing to new problems. They can choose and parameterize suitable filter structures. In particular, they can design adaptive filters according to the minimum mean squared error (MMSE) criterion and develop an efficient implementation, e.g. based on the LMS or RLS algorithm. Furthermore, the students are able to apply methods of spectrum estimation and to take the effects of a limited observation window into account.</p> <p><b>Personal Competence</b></p> <p><i>Social Competence</i> The students can jointly solve specific problems.</p> <p><i>Autonomy</i> The students are able to acquire relevant information from appropriate literature sources. They can control their level of knowledge during the lecture period by solving tutorial problems, software tools, clicker system.</p>			
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70			
<b>Credit points</b>	6			
<b>Course achievement</b>	None			
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	90 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Control and Power Systems Engineering: Elective Compulsory Computer Science in Engineering: Specialisation II. Engineering Science: Elective Compulsory Information and Communication Systems: Specialisation Communication Systems, Focus Signal Processing: Elective Compulsory Mechanical Engineering and Management: Specialisation Mechatronics: Elective Compulsory Mechatronics: Specialisation Intelligent Systems and Robotics: Elective Compulsory Microelectronics and Microsystems: Specialisation Communication and Signal Processing: Elective Compulsory Theoretical Mechanical Engineering: Specialisation Robotics and Computer Science: Elective Compulsory			

Course L0446: Digital Signal Processing and Digital Filters	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42
<b>Lecturer</b>	Prof. Gerhard Bauch
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Transforms of discrete-time signals:               <ul style="list-style-type: none"> <li>◦ Discrete-time Fourier Transform (DTFT)</li> <li>◦ Discrete Fourier-Transform (DFT), Fast Fourier Transform (FFT)</li> <li>◦ Z-Transform</li> </ul> </li> <li>• Correspondence of continuous-time and discrete-time signals, sampling, sampling theorem</li> <li>• Fast convolution, Overlap-Add-Method, Overlap-Save-Method</li> <li>• Fundamental structures and basic types of digital filters</li> <li>• Characterization of digital filters using pole-zero plots, important properties of digital filters</li> <li>• Quantization effects</li> <li>• Design of linear-phase filters</li> <li>• Fundamentals of stochastic signal processing and adaptive filters               <ul style="list-style-type: none"> <li>◦ MMSE criterion</li> <li>◦ Wiener Filter</li> <li>◦ LMS- and RLS-algorithm</li> </ul> </li> <li>• Traditional and parametric methods of spectrum estimation</li> </ul>
<b>Literature</b>	<p>K.-D. Kammeyer, K. Kroschel: Digitale Signalverarbeitung. Vieweg Teubner.</p> <p>V. Oppenheim, R. W. Schaffer, J. R. Buck: Zeitdiskrete Signalverarbeitung. Pearson StudiumA. V.</p> <p>W. Hess: Digitale Filter. Teubner.</p> <p>Oppenheim, R. W. Schaffer: Digital signal processing. Prentice Hall.</p> <p>S. Haykin: Adaptive filter theory.</p> <p>L. B. Jackson: Digital filters and signal processing. Kluwer.</p> <p>T.W. Parks, C.S. Burrus: Digital filter design. Wiley.</p>

Course L0447: Digital Signal Processing and Digital Filters	
<b>Typ</b>	Recitation Section (large)
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Gerhard Bauch
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1249: Medical Imaging			
<b>Courses</b>			
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b> <b>CP</b>
Medical Imaging (L1694)		Lecture	2                  3
Medical Imaging (L1695)		Recitation Section (small)	2                  3
<b>Module Responsible</b>	Prof. Tobias Knopp		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	Basic knowledge in linear algebra, numerics, and signal processing		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b>			
<i>Knowledge</i>	After successful completion of the module, students are able to describe reconstruction methods for different tomographic imaging modalities such as computed tomography and magnetic resonance imaging. They know the necessary basics from the fields of signal processing and inverse problems and are familiar with both analytical and iterative image reconstruction methods. The students have a deepened knowledge of the imaging operators of computed tomography and magnetic resonance imaging.		
<i>Skills</i>	The students are able to implement reconstruction methods and test them using tomographic measurement data. They can visualize the reconstructed images and evaluate the quality of their data and results. In addition, students can estimate the temporal complexity of imaging algorithms.		
<b>Personal Competence</b>			
<i>Social Competence</i>	Students can work on complex problems both independently and in teams. They can exchange ideas with each other and use their individual strengths to solve the problem.		
<i>Autonomy</i>	Students are able to independently investigate a complex problem and assess which competencies are required to solve it.		
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56		
<b>Credit points</b>	6		
<b>Course achievement</b>	None		
<b>Examination</b>	Written exam		
<b>Examination duration and scale</b>	90 min		
<b>Assignment for the Following Curricula</b>	Computer Science: Specialisation II: Intelligence Engineering: Elective Compulsory Electrical Engineering: Specialisation Medical Technology: Elective Compulsory Computer Science in Engineering: Specialisation I. Computer Science: Elective Compulsory Interdisciplinary Mathematics: Specialisation Computational Methods in Biomedical Imaging: Compulsory Microelectronics and Microsystems: Specialisation Communication and Signal Processing: Elective Compulsory Theoretical Mechanical Engineering: Specialisation Bio- and Medical Technology: Elective Compulsory		

Course L1694: Medical Imaging	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Tobias Knopp
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Overview about different imaging methods</li> <li>• Signal processing</li> <li>• Inverse problems</li> <li>• Computed tomography</li> <li>• Magnetic resonance imaging</li> <li>• Compressed Sensing</li> <li>• Magnetic particle imaging</li> </ul>
<b>Literature</b>	<b>Bildgebende Verfahren in der Medizin</b> ; O. Dössel; Springer, Berlin, 2000  <b>Bildgebende Systeme für die medizinische Diagnostik</b> ; H. Morneburg (Hrsg.); Publicis MCD, München, 1995  <b>Introduction to the Mathematics of Medical Imaging</b> ; C. L.Epstein; Siam, Philadelphia, 2008  <b>Medical Image Processing, Reconstruction and Restoration</b> ; J. Jan; Taylor and Francis, Boca Raton, 2006  <b>Principles of Magnetic Resonance Imaging</b> ; Z.-P. Liang and P. C. Lauterbur; IEEE Press, New York, 1999

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<b>Course L1695: Medical Imaging</b>	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Tobias Knopp
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

## Specialization Embedded Systems

### Module M0791: Computer Architecture

#### Courses

Title	Typ	Hrs/wk	CP
Computer Architecture (L0793)	Lecture	2	3
Computer Architecture (L0794)	Project-/problem-based Learning	2	2
Computer Architecture (L1864)	Recitation Section (small)	1	1
<b>Module Responsible</b>	Prof. Heiko Falk		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	Module "Computer Engineering"		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b>	<p><i>Knowledge</i> This module presents advanced concepts from the discipline of computer architecture. In the beginning, a broad overview over various programming models is given, both for general-purpose computers and for special-purpose machines (e.g., signal processors). Next, foundational aspects of the micro-architecture of processors are covered. Here, the focus particularly lies on the so-called pipelining and the methods used for the acceleration of instruction execution used in this context. The students get to know concepts for dynamic scheduling, branch prediction, superscalar execution of machine instructions and for memory hierarchies.</p> <p><i>Skills</i> The students are able to describe the organization of processors. They know the different architectural principles and programming models. The students examine various structures of pipelined processor architectures and are able to explain their concepts and to analyze them w.r.t. criteria like, e.g., performance or energy efficiency. They evaluate different structures of memory hierarchies, know parallel computer architectures and are able to distinguish between instruction- and data-level parallelism.</p> <p><b>Personal Competence</b></p> <p><i>Social Competence</i> Students are able to solve similar problems alone or in a group and to present the results accordingly.</p> <p><i>Autonomy</i> Students are able to acquire new knowledge from specific literature and to associate this knowledge with other classes.</p>		
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70		
<b>Credit points</b>	6		
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b> <b>Description</b>
	No	15 %	Subject theoretical and practical work
<b>Examination</b>	Written exam		
<b>Examination duration and scale</b>	90 minutes, contents of course and 4 attestations from the PBL "Computer architecture"		
<b>Assignment for the Following Curricula</b>	General Engineering Science (German program, 7 semester): Specialisation Computer Science: Elective Compulsory Computer Science: Specialisation I. Computer and Software Engineering: Elective Compulsory Aircraft Systems Engineering: Core Qualification: Elective Compulsory Computer Science in Engineering: Specialisation I. Computer Science: Elective Compulsory Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory		

Course L0793: Computer Architecture	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Heiko Falk
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction</li> <li>• VHDL Basics</li> <li>• Programming Models</li> <li>• Realization of Elementary Data Types</li> <li>• Dynamic Scheduling</li> <li>• Branch Prediction</li> <li>• Superscalar Machines</li> <li>• Memory Hierarchies</li> </ul> <p>The theoretical tutorials amplify the lecture's content by solving and discussing exercise sheets and thus serve as exam preparation. Practical aspects of computer architecture are taught in the FPGA-based PBL on computer architecture whose attendance is mandatory.</p>
<b>Literature</b>	<ul style="list-style-type: none"> <li>• D. Patterson, J. Hennessy. Rechnerorganisation und -entwurf. Elsevier, 2005.</li> <li>• A. Tanenbaum, J. Goodman. Computerarchitektur. Pearson, 2001.</li> </ul>

Course L0794: Computer Architecture	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Heiko Falk
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Course L1864: Computer Architecture	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Heiko Falk
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1749: Energy Efficiency in Embedded Systems				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Energy Efficiency in Embedded Systems (L2870)		Lecture	2	3
Energy Efficiency in Embedded Systems (L2872)		Project-/problem-based Learning	2	2
Energy Efficiency in Embedded Systems (L2871)		Recitation Section (large)	1	1
<b>Module Responsible</b>	Prof. Ulf Kulau			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>• Computer Engineering (mandatory)</li> <li>• Programming Skills in C (mandatory)</li> <li>• Computer Architecture (recommended)</li> </ul>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i> <b>Motivation:</b> In the field of computer science we have only limited possibilities to influence the efficiency of the hardware directly, respectively we are dependent on the manufacturers (e.g. of microcontrollers). However, in order to exploit the full potential of the hardware we are given at the system level, we need a deeper understanding of the background, processes and mechanisms of power dissipation in embedded systems. Where does the power dissipation come from, what happens at the hardware level, what mechanisms can I use directly/indirectly, what is the tradeoff between flexibility and efficiency..... are only a few questions, which will be elaborated and discussed in this event.</p> <p><b>Contents of teaching:</b></p> <ul style="list-style-type: none"> <li>• Motivation and power dissipation on semiconductor level</li> <li>• Power dissipation of digital circuits, in particular CMOS</li> <li>• Power Management in Hard- and Software (Sleep Modes, DVS, FS, Undervolting)</li> <li>• Energy efficient system design (applications)</li> <li>• Energy Harvesting and Transiently Powered Computing (TPC)</li> </ul> <p><i>Skills</i> Upon completion of this module, students will have a deeper understanding of hardware and software mechanisms for evaluating and developing energy-efficient embedded systems</p> <ul style="list-style-type: none"> <li>• They have a deeper understanding of the electrotechnical basics of power dissipation in digital systems</li> <li>• They can analyze the power dissipation of systems at any level and apply appropriate methods to increase efficiency</li> <li>• They can use a variety of standard techniques to achieve "Energy Efficiency by Design"</li> <li>• They can model, evaluate as well as implement energy-autonomous systems</li> </ul>			
<b>Personal Competence</b>	<p><i>Social Competence</i> As part of the module, concepts learned in the lecture will be implemented on a hardware platform within small groups. Students learn to work in a team and to develop solutions together. Specific tasks are worked on within the group, whereby cross-group collaboration (exchange) also takes place. The second part is a challenge-based project in which the groups find the most energy-efficient solutions possible in healthy competition with each other. This strengthens the cohesion in the groups and reinforces mutual motivation, support and creativity.</p> <p><i>Autonomy</i> After completing this module, students will be able to independently develop, optimize and evaluate solutions for embedded systems based on the knowledge they have acquired and further technical literature.</p>			
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70			
<b>Credit points</b>	6			
<b>Course achievement</b>	None			
<b>Examination</b>	Oral exam			
<b>Examination duration and scale</b>	25 min			
<b>Assignment for the Following Curricula</b>	Computer Science: Specialisation I. Computer and Software Engineering: Elective Compulsory Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory			



Course L2870: Energy Efficiency in Embedded Systems	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Ulf Kulau
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	<p><b>Motivation:</b></p> <p>In the field of computer science we have only limited possibilities to influence the efficiency of the hardware directly, respectively we are dependent on the manufacturers (e.g. of microcontrollers). However, in order to exploit the full potential of the hardware we are given at the system level, we need a deeper understanding of the background, processes and mechanisms of power dissipation in embedded systems. Where does the power dissipation come from, what happens at the hardware level, what mechanisms can I use directly/indirectly, what is the tradeoff between flexibility and efficiency,..... are only a few questions, which will be elaborated and discussed in this event.</p> <p><b>Contents of teaching:</b></p> <ul style="list-style-type: none"> <li>• Motivation and power dissipation on semiconductor level</li> <li>• Power dissipation of digital circuits, in particular CMOS</li> <li>• Power Management in Hard- and Software (Sleep Modes, DVS, FS, Undervolting)</li> <li>• Energy efficient system design (applications)</li> <li>• Energy Harvesting and Transiently Powered Computing (TPC)</li> </ul>
<b>Literature</b>	<p>DE: Die Vorlesung basiert auf einer Vielzahl von Quellen, welche in [1.] angegeben sind.</p> <p>ENG: The lecture is based on multiple sources which are listed in [1.].</p> <ol style="list-style-type: none"> <li>1. Kulau, Ulf: <b>Course: Energy Efficiency in Embedded Systems-A System-Level Perspective for Computer Scientists</b>, EWME, 2018.</li> <li>2. Harris, David, and N. Weste: <b>CMOS VLSI Design</b> ed., Pearson Education, 2010</li> <li>3. Rabaey, Jan: <b>Low Power Design Essentials</b> (Integrated Circuits and Systems), Springer, 2009</li> </ol>

Course L2872: Energy Efficiency in Embedded Systems	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Ulf Kulau
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	<p>In this project-based exercise, the learned aspects for achieving energy-efficient embedded systems are implemented and consolidated in practical environments in a small project. First, a tool set for the implementation of energy efficiency mechanisms is implemented in common exercises by means of defined tasks. In the second part, a challenge-based exercise is carried out in which a system that is as efficient as possible is to be implemented independently. A system based on an AVR micro-controller is used, which can be operated autonomously by a Solar-Energy Harvester.</p> <ol style="list-style-type: none"> <li>1. Task phase: 6 "hands-on" tasks to gain experience and to create a SW library.</li> <li>2. Project phase: Implementation of an energy autonomous system with the goal of highest possible energy efficiency (Challenge)</li> </ol>
<b>Literature</b>	

<b>Course L2871: Energy Efficiency in Embedded Systems</b>	
<b>Typ</b>	Recitation Section (large)
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Ulf Kulau
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	<p>In the lecture hall exercise, the theoretical basics taught in the lecture are deepened. This is done through in-depth discussion of relevant aspects, but also through calculation examples, in which a deeper understanding of the topic of energy efficiency in embedded systems is gained. Exercises will be distributed in advance and solutions will be presented in the lecture hall exercise. Contents of the exercise are as follows:</p> <ul style="list-style-type: none"> <li>• Basics and calculation of power dissipation on semiconductor</li> <li>• Power dissipation of CMOS using the example of an inverter</li> <li>• Influence of the activity factor and external components</li> <li>• DVS and scheduling</li> <li>• Evaluation to show the benefit of undervolting</li> <li>• Aspects of energy harvesting (MPPT)</li> <li>•</li> </ul>
<b>Literature</b>	

Module M0924: Software for Embedded Systems				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Software for Embedded Systems (L1069)		Lecture	2	3
Software for Embedded Systems (L1070)		Recitation Section (small)	3	3
<b>Module Responsible</b>	Prof. Bernd-Christian Renner			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>• Very Good knowledge and practical experience in programming in the C language</li> <li>• Basic knowledge in software engineering</li> <li>• Basic understanding of assembly language</li> </ul>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i> Students know the basic principles and procedures of software engineering for embedded systems. They are able to describe the usage and pros of event based programming using interrupts. They know the components and functions of a concrete microcontroller. The participants explain requirements of real time systems. They know at least three scheduling algorithms for real time operating systems including their pros and cons.</p> <p><i>Skills</i> Students build interrupt-based programs for a concrete microcontroller. They build and use a preemptive scheduler. They use peripheral components (timer, ADC, EEPROM) to realize complex tasks for embedded systems. To interface with external components they utilize serial protocols.</p> <p><b>Personal Competence</b></p> <p><i>Social Competence</i></p> <p><i>Autonomy</i></p>			
<b>Workload in Hours</b>				
<b>Credit points</b>				
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>	<b>Description</b>
	No	10 %	Attestation	
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	90 min			
<b>Assignment for the Following Curricula</b>	Computer Science: Specialisation I. Computer and Software Engineering: Elective Compulsory Electrical Engineering: Specialisation Information and Communication Systems: Elective Compulsory Information and Communication Systems: Specialisation Communication Systems, Focus Software: Elective Compulsory Mechatronics: Technical Complementary Course: Elective Compulsory Mechatronics: Specialisation Intelligent Systems and Robotics: Elective Compulsory Mechatronics: Specialisation System Design: Elective Compulsory Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory			

Course L1069: Software for Embedded Systems	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Bernd-Christian Renner
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• General-Purpose Processors</li> <li>• Programming the Atmel AVR</li> <li>• Interrupts</li> <li>• C for Embedded Systems</li> <li>• Standard Single Purpose Processors: Peripherals</li> <li>• Finite-State Machines</li> <li>• Memory</li> <li>• Operating Systems for Embedded Systems</li> <li>• Real-Time Embedded Systems</li> <li>• Boot loader and Power Management</li> </ul>
<b>Literature</b>	<ol style="list-style-type: none"> <li>1. Embedded System Design, F. Vahid and T. Givargis, John Wiley</li> <li>2. Programming Embedded Systems: With C and Gnu Development Tools, M. Barr and A. Massa, O'Reilly</li> <li>3. C und C++ für Embedded Systems, F. Bollow, M. Homann, K. Köhn, MITP</li> <li>4. The Art of Designing Embedded Systems, J. Ganssle, Newnes</li> <li>5. Mikrocomputertechnik mit Controllern der Atmel AVR-RISC-Familie, G. Schmitt, Oldenbourg</li> <li>6. Making Embedded Systems: Design Patterns for Great Software, E. White, O'Reilly</li> </ol>

<b>Course L1070: Software for Embedded Systems</b>	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	3
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 48, Study Time in Lecture 42
<b>Lecturer</b>	Prof. Bernd-Christian Renner
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1400: Design of Dependable Systems				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Designing Dependable Systems (L2000)		Lecture	2	3
Designing Dependable Systems (L2001)		Recitation Section (small)	2	3
<b>Module Responsible</b>	Prof. Görschwin Fey			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Basic knowledge about data structures and algorithms			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i> In the following "dependable" summarizes the concepts Reliability, Availability, Maintainability, Safety and Security.</p> <p>Knowledge about approaches for designing dependable systems, e.g.,</p> <ul style="list-style-type: none"> <li>• Structural solutions like modular redundancy</li> <li>• Algorithmic solutions like handling byzantine faults or checkpointing</li> </ul> <p>Knowledge about methods for the analysis of dependable systems</p> <p><i>Skills</i> Ability to implement dependable systems using the above approaches.</p> <p>Ability to analyzs the dependability of systems using the above methods for analysis.</p> <p><b>Personal Competence</b></p> <p><i>Social Competence</i> Students</p> <ul style="list-style-type: none"> <li>• discuss relevant topics in class and</li> <li>• present their solutions orally.</li> </ul> <p><i>Autonomy</i> Using accompanying material students independently learn in-depth relations between concepts explained in the lecture and additional solution strategies.</p>			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56			
<b>Credit points</b>	6			
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>	<b>Description</b>
	Yes	None	Subject theoretical and practical work	Die Lösung einer Aufgabe ist Zulassungsvoraussetzung für die Prüfung. Die Aufgabe wird in Vorlesung und Übung definiert.
<b>Examination</b>	Oral exam			
<b>Examination duration and scale</b>	30 min			
<b>Assignment for the Following Curricula</b>	Computer Science: Specialisation I. Computer and Software Engineering: Elective Compulsory Computer Science in Engineering: Specialisation I. Computer Science: Elective Compulsory Information and Communication Systems: Specialisation Secure and Dependable IT Systems: Elective Compulsory Mechatronics: Specialisation System Design: Elective Compulsory Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory			

Course L2000: Designing Dependable Systems	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Görschwin Fey
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	<p>Description</p> <p>The term dependability comprises various aspects of a system. These are typically:</p> <ul style="list-style-type: none"> <li>• Reliability</li> <li>• Availability</li> <li>• Maintainability</li> <li>• Safety</li> <li>• Security</li> </ul> <p>This makes dependability a core aspect that has to be considered early in system design, no matter whether software, embedded systems or full scale cyber-physical systems are considered.</p> <p>Contents</p> <p>The module introduces the basic concepts for the design and the analysis of dependable systems. Design examples for getting practical hands-on-experience in dependable design techniques. The module focuses towards embedded systems. The following topics are covered:</p> <ul style="list-style-type: none"> <li>• Modelling</li> <li>• Fault Tolerance</li> <li>• Design Concepts</li> <li>• Analysis Techniques</li> </ul>
<b>Literature</b>	

Course L2001: Designing Dependable Systems	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Görschwin Fey
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1772: Smart Sensors			
<b>Courses</b>			
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b> <b>CP</b>
Smart Sensors (L2904)		Lecture	2                  2
Smart Sensors Lab (L2905)		Project-/problem-based Learning	3                  4
<b>Module Responsible</b>	Prof. Ulf Kulau		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b> <i>Knowledge</i> <i>Skills</i>			
<b>Personal Competence</b> <i>Social Competence</i> <i>Autonomy</i>			
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70		
<b>Credit points</b>	6		
<b>Course achievement</b>	None		
<b>Examination</b>	Oral exam		
<b>Examination duration and scale</b>	25 min		
<b>Assignment for the Following Curricula</b>	Computer Science: Specialisation I. Computer and Software Engineering: Elective Compulsory Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory		

Course L2904: Smart Sensors	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Ulf Kulau
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	
<b>Literature</b>	

Course L2905: Smart Sensors Lab	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	3
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42
<b>Lecturer</b>	Prof. Ulf Kulau
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	
<b>Literature</b>	

Module M0803: Embedded Systems				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Embedded Systems (L0805)		Lecture	3	3
Embedded Systems (L2938)		Project-/problem-based Learning	1	1
Embedded Systems (L0806)		Recitation Section (small)	1	2
<b>Module Responsible</b>	Prof. Heiko Falk			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Computer Engineering			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i> Embedded systems can be defined as information processing systems embedded into enclosing products. This course teaches the foundations of such systems. In particular, it deals with an introduction into these systems (notions, common characteristics) and their specification languages (models of computation, hierarchical automata, specification of distributed systems, task graphs, specification of real-time applications, translations between different models).</p> <p>Another part covers the hardware of embedded systems: Sensors, A/D and D/A converters, real-time capable communication hardware, embedded processors, memories, energy dissipation, reconfigurable logic and actuators. The course also features an introduction into real-time operating systems, middleware and real-time scheduling. Finally, the implementation of embedded systems using hardware/software co-design (hardware/software partitioning, high-level transformations of specifications, energy-efficient realizations, compilers for embedded processors) is covered.</p> <p><i>Skills</i> After having attended the course, students shall be able to realize simple embedded systems. The students shall realize which relevant parts of technological competences to use in order to obtain a functional embedded systems. In particular, they shall be able to compare different models of computations and feasible techniques for system-level design. They shall be able to judge in which areas of embedded system design specific risks exist.</p>			
<b>Personal Competence</b>	<p><i>Social Competence</i> Students are able to solve similar problems alone or in a group and to present the results accordingly.</p> <p><i>Autonomy</i> Students are able to acquire new knowledge from specific literature and to associate this knowledge with other classes.</p>			
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70			
<b>Credit points</b>	6			
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>	<b>Description</b>
	Yes	10 %	Subject	theoretical and practical work
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	90 minutes, contents of course and labs			
<b>Assignment for the Following Curricula</b>	General Engineering Science (German program, 7 semester): Specialisation Computer Science: Compulsory Computer Science: Specialisation I. Computer and Software Engineering: Elective Compulsory Electrical Engineering: Core Qualification: Elective Compulsory Engineering Science: Specialisation Mechatronics: Elective Compulsory Engineering Science: Specialisation Electrical Engineering: Elective Compulsory Aircraft Systems Engineering: Core Qualification: Elective Compulsory General Engineering Science (English program, 7 semester): Specialisation Mechatronics: Elective Compulsory Computer Science in Engineering: Core Qualification: Compulsory Mechatronics: Specialisation System Design: Elective Compulsory Mechatronics: Specialisation Intelligent Systems and Robotics: Elective Compulsory Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory			



Course L0805: Embedded Systems	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 48, Study Time in Lecture 42
<b>Lecturer</b>	Prof. Heiko Falk
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction</li> <li>• Specifications and Modeling</li> <li>• Embedded/Cyber-Physical Systems Hardware</li> <li>• System Software</li> <li>• Evaluation and Validation</li> <li>• Mapping of Applications to Execution Platforms</li> <li>• Optimization</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>• Peter Marwedel. Embedded System Design - Embedded Systems Foundations of Cyber-Physical Systems. 2<sup>nd</sup> Edition, Springer, 2012., Springer, 2012.</li> </ul>

Course L2938: Embedded Systems	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Heiko Falk
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction</li> <li>• Specifications and Modeling</li> <li>• Embedded/Cyber-Physical Systems Hardware</li> <li>• System Software</li> <li>• Evaluation and Validation</li> <li>• Mapping of Applications to Execution Platforms</li> <li>• Optimization</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>• Peter Marwedel. Embedded System Design - Embedded Systems Foundations of Cyber-Physical Systems. 2<sup>nd</sup> Edition, Springer, 2012., Springer, 2012.</li> </ul>

Course L0806: Embedded Systems	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 46, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Heiko Falk
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1771: Research Based Learning - Smart Sensing Applications				
<b>Courses</b>				
<b>Title</b>	Research Based Learning - Smart Sensing Applications (L2903)		<b>Typ</b>	Project-/problem-based Learning
			<b>Hrs/wk</b>	4
			<b>CP</b>	6
<b>Module Responsible</b>	Prof. Ulf Kulau			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	<ul style="list-style-type: none"> <li>• Embedded Systems</li> <li>• Smart Sensors</li> <li>• Technische Informatik</li> </ul>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i></p> <ul style="list-style-type: none"> <li>• Involvement of students in real research topic.</li> <li>• Topics may change depending on timeliness. BCG offers itself as a topic: It is relevant, current and interdisciplinary.</li> <li>• Create interdisciplinary connection points / colloquium with project-related, but also with institutes/universities from other disciplines</li> <li>• Generate or provide data sets</li> <li>• Find methods derive develop for integrated signal processing for the respective project reference</li> <li>• Soft skills in the area of communication &amp; interdisciplinarity (learning to understand each other's language)</li> </ul> <p><i>Skills</i></p> <p>After completing the module, students are able to better understand and actively accompany scientific processes. Thereby, the involvement in a real research project (topic depending on topicality) is a high motivation and is given. Students receive a general understanding of the respective research project, iudem basics and backgrounds are conveyed. In order to be able to provide own research contributions within the set framework, methods for scientific practice are taught.</p> <ul style="list-style-type: none"> <li>• Teaching of fundamentals (interdisciplinary, smart sensors / other disciplines)</li> <li>• Design of experiments / hypotheses (framework is given -&gt; methodology should be taught)</li> <li>• Execution of experiments (execution of experiments / generation of measurement data)</li> <li>• Scientific evaluation of the data</li> <li>• Presentation of results Discussion of further utilization (publication if necessary)</li> </ul> <p><b>Personal Competence</b></p> <p><i>Social Competence</i></p> <p>The work is done in groups and close cooperation and coordination within the individual teams is required. Through the interface "sensors" it is possible to select topics with a strong interdisciplinary share. Mutual understanding (finding a common language) is learned through this. Since real scientific problems are to be investigated, students acquire the ability to implement good scientific practice in a disciplined, objective and critical manner.</p> <p><i>Autonomy</i></p> <p>After completing the module, students will be able to independently plan and carry out scientific processes. In group work, organization, idea generation, derivation of hypotheses and thought processes are to be independently moderated and carried out.</p>			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56			
<b>Credit points</b>	6			
<b>Course achievement</b>	None			
<b>Examination</b>	Written elaboration			
<b>Examination duration and scale</b>	Paper including the achieved results			
<b>Assignment for the Following Curricula</b>	Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory			

Course L2903: Research Based Learning - Smart Sensing Applications	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	4
<b>CP</b>	6
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56
<b>Lecturer</b>	Prof. Ulf Kulau
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	
<b>Literature</b>	

Module M0925: Digital Circuit Design			
<b>Courses</b>			
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b> <b>CP</b>
Digital Circuit Design (L0698)		Lecture	2                  3
Advanced Digital Circuit Design (L0699)		Lecture	2                  3
<b>Module Responsible</b>	Prof. Matthias Kuhl		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b> <i>Knowledge</i> <i>Skills</i>			
<b>Personal Competence</b> <i>Social Competence</i> <i>Autonomy</i>			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56		
<b>Credit points</b>	6		
<b>Course achievement</b>	None		
<b>Examination</b>	Oral exam		
<b>Examination duration and scale</b>	40 min		
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory International Management and Engineering: Specialisation II. Electrical Engineering: Elective Compulsory Mechanical Engineering and Management: Specialisation Mechatronics: Elective Compulsory Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory		

Course L0698: Digital Circuit Design	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Volkhard Klinger
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	
<b>Literature</b>	

Course L0699: Advanced Digital Circuit Design	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Volkhard Klinger
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	
<b>Literature</b>	

Module M1687: Selected Aspects of Embedded Systems			
<b>Courses</b>			
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b> <b>CP</b>
Selected Aspects of Embedded Systems (L2676)		Lecture	3                  4
Selected Aspects of Embedded Systems (L2677)		Recitation Section (small)	1                  2
<b>Module Responsible</b>	Prof. Hoc Khiem Trieu		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b> <i>Knowledge</i> <i>Skills</i>			
<b>Personal Competence</b> <i>Social Competence</i> <i>Autonomy</i>			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56		
<b>Credit points</b>	6		
<b>Course achievement</b>	None		
<b>Examination</b>	Oral exam		
<b>Examination duration and scale</b>	30 min		
<b>Assignment for the Following Curricula</b>	Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory		

Course L2676: Selected Aspects of Embedded Systems	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42
<b>Lecturer</b>	Dozenten des SD E
<b>Language</b>	EN
<b>Cycle</b>	WiSe/SoSe
<b>Content</b>	
<b>Literature</b>	

Course L2677: Selected Aspects of Embedded Systems	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 46, Study Time in Lecture 14
<b>Lecturer</b>	Dozenten des SD E
<b>Language</b>	EN
<b>Cycle</b>	WiSe/SoSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M0910: Advanced System-on-Chip Design (Lab)				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Advanced System-on-Chip Design (L1061)		Project-/problem-based Learning	3	6
<b>Module Responsible</b>	Prof. Heiko Falk			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Successful completion of the practical FPGA lab of module "Computer Architecture" is a mandatory prerequisite.			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<p><i>Knowledge</i> This module provides in-depth, hands-on experience on advanced concepts of computer architecture. Using the Hardware Description Language VHDL and using reconfigurable FPGA hardware boards, students learn how to design complex computer systems (so-called systems-on-chip, SoCs), that are commonly found in the domain of embedded systems, in actual hardware.</p> <p>Starting with a simple processor architecture, the students learn to how realize instruction-processing of a computer processor according to the principle of pipelining. They implement different styles of cache-based memory hierarchies, examine strategies for dynamic scheduling of machine instructions and for branch prediction, and finally construct a complex MPSoC system (multi-processor system-on-chip) that consists of multiple processor cores that are connected via a shared bus.</p> <p><i>Skills</i> Students will be able to analyze, how highly specific and individual computer systems can be constructed using a library of given standard components. They evaluate the interferences between the physical structure of a computer system and the software executed thereon. This way, they will be enabled to estimate the effects of design decision at the hardware level on the performance of the entire system, to evaluate the whole and complex system and to propose design options to improve a system.</p> <p><b>Personal Competence</b></p> <p><i>Social Competence</i> Students are able to solve similar problems alone or in a group and to present the results accordingly.</p> <p><i>Autonomy</i> Students are able to acquire new knowledge from specific literature, to transform this knowledge into actual implementations of complex hardware structures, and to associate this knowledge with contents of other classes.</p>			
<b>Workload in Hours</b>				
<b>Credit points</b>				
<b>Course achievement</b>				
<b>Examination</b>	Subject theoretical and practical work			
<b>Examination duration and scale</b>	VHDL Codes and FPGA-based implementations			
<b>Assignment for the Following Curricula</b>	Computer Science: Specialisation I. Computer and Software Engineering: Elective Compulsory Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory			
Course L1061: Advanced System-on-Chip Design				
<b>Typ</b>	Project-/problem-based Learning			
<b>Hrs/wk</b>	3			
<b>CP</b>	6			
<b>Workload in Hours</b>	Independent Study Time 138, Study Time in Lecture 42			
<b>Lecturer</b>	Prof. Heiko Falk			
<b>Language</b>	DE/EN			
<b>Cycle</b>	WiSe			
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction into fundamental technologies (FPGAs, MIPS single-cycle machine)</li> <li>• Pipelined instruction execution</li> <li>• Cache-based memory hierarchies</li> <li>• Busses and their arbitration</li> <li>• Multi-Processor Systems-on-Chip</li> <li>• Optional: Advanced pipelining concepts (dynamic scheduling, branch prediction)</li> </ul>			
<b>Literature</b>	<ul style="list-style-type: none"> <li>• D. Patterson, J. Hennessy. Rechnerorganisation und -entwurf. Elsevier, 2005.</li> <li>• A. Tanenbaum, J. Goodman. Computerarchitektur. Pearson, 2001.</li> <li>• A. Clements. The Principles of Computer Hardware. 3. Auflage, Oxford University Press, 2000.</li> </ul>			

## Specialization Microelectronics Complements

Students of the specialization Microelectronics Complements expand their knowledge towards the application of microelectronics and microsystems for medical use, the processing of digital signals, the development and design of highly complex integrated systems and networks for optical communication. Thus, they strengthen their knowledge by analyzing practical applications and link it up with the requirements of technical realizations.

Students have to choose lectures with a total of 18 credit points from the catalog of this specialization.

Module M0925: Digital Circuit Design	
<b>Courses</b>	
<b>Title</b>	<b>Typ</b>
Digital Circuit Design (L0698)	Lecture
Advanced Digital Circuit Design (L0699)	Lecture
<b>Module Responsible</b>	Prof. Matthias Kuhl
<b>Admission Requirements</b>	None
<b>Recommended Previous Knowledge</b>	
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results
<b>Professional Competence</b> <i>Knowledge</i> <i>Skills</i>	
<b>Personal Competence</b> <i>Social Competence</i> <i>Autonomy</i>	
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56
<b>Credit points</b>	6
<b>Course achievement</b>	None
<b>Examination</b>	Oral exam
<b>Examination duration and scale</b>	40 min
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory International Management and Engineering: Specialisation II. Electrical Engineering: Elective Compulsory Mechanical Engineering and Management: Specialisation Mechatronics: Elective Compulsory Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory Microelectronics and Microsystems: Specialisation Embedded Systems: Elective Compulsory

Course L0698: Digital Circuit Design	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Volkhard Klinger
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	
<b>Literature</b>	

Course L0699: Advanced Digital Circuit Design	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Volkhard Klinger
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	
<b>Literature</b>	

Module M1611: Silicon Photonics			
Courses			
Title	Typ	Hrs/wk	CP
Silicon Photonics (L2408)	Lecture	2	4
Silicon Photonics (L2418)	Project-/problem-based Learning	2	2
<b>Module Responsible</b>	Dr. Timo Lipka		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	Basics in physics, optics, microsystem and semiconductor technology		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b>	<p><i>Knowledge</i> The students know the fundamentals of silicon photonics and about the most important and commonly used materials and fabrication techniques.</p> <p>Students are able</p> <ul style="list-style-type: none"> <li>• to explain the basic principles of silicon photonics technology and to discuss theoretical and practical aspects</li> <li>• to describe photonic circuit devices and their working principle</li> <li>• to describe the manufacturing of silicon photonic devices and to discuss in details the relevant fabrication processes, process flows and the impact thereof on the fabrication of photonic integrated circuit components</li> </ul> <p><i>Skills</i> Students are capable to</p> <ul style="list-style-type: none"> <li>• analyze the feasibility of integrated photonic circuit components</li> <li>• choose appropriate tools and methods to design them</li> <li>• develop process flows for the fabrication</li> </ul> <p><b>Personal Competence</b></p> <p><i>Social Competence</i> Students are able to prepare and perform their lab experiments in team work as well as to present and discuss the results in front of audience.</p> <p><i>Autonomy</i> none</p>		
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56		
<b>Credit points</b>	6		
<b>Course achievement</b>	None		
<b>Examination</b>	Oral exam		
<b>Examination duration and scale</b>	30 min		
<b>Assignment for the Following Curricula</b>	Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory		

Course L2408: Silicon Photonics	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 92, Study Time in Lecture 28
<b>Lecturer</b>	Dr. Timo Lipka
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction (historical view and trends in der Silicon Photonics)</li> <li>• Fabrication Technology (SOI-Wafer, Deposition, Sputtering and Evaporation, Epitaxy, MOCVD, Lithography)</li> <li>• Planar Waveguide Fundamentals</li> <li>• Optical Materials in silicon Photonics</li> <li>• Waveguide Types (Loss Mechanisms, Dispersion and Polarisation in Waveguides)</li> <li>• Coupling of Silicon Photonic Devices and Systems</li> <li>• Silicon Photonic Circuit Devices and Building Blocks (Passive Devices: Resonators, Interferometers, Mode Converters, Power Splitters, Gratings, Polarizers and Rotators)</li> <li>• Material fundamentals and components for tuning and switching</li> <li>• Integration of active Devices (Laser, Detector, Modulators)</li> <li>• Photonics and Electronics Integration</li> <li>• Photonic Interconnects</li> <li>• Optical Multiplexing</li> <li>• Switch Fabrics and Routers</li> <li>• Silicon Photonics for Sensing</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>• Graham T. Reed, Andrew Knights, Silicon Photonics - An Introduction, John Wiley &amp; Sons Ltd (2004)</li> <li>• Clifford R. Pollocka and Michal Lipson, Integrated Photonics, Springer-Verlag (2003)</li> <li>• Sami Franssila, Introduction to microfabrication, Chichester, West Sussex Wiley (2010)</li> <li>• Dominik G. Rabus, Integrated Ring Resonators: The Compendium, in Springer Series in Optical Sciences (2007)</li> </ul>

Course L2418: Silicon Photonics	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	2
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 32, Study Time in Lecture 28
<b>Lecturer</b>	Dr. Timo Lipka
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course



Module M0769: EMC I: Coupling Mechanisms, Countermeasures and Test Procedures				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
EMC I: Coupling Mechanisms, Countermeasures, and Test Procedures (L0743)		Lecture	3	4
EMC I: Coupling Mechanisms, Countermeasures, and Test Procedures (L0744)		Recitation Section (small)	1	1
EMC I: Coupling Mechanisms, Countermeasures, and Test Procedures (L0745)		Practical Course	1	1
<b>Module Responsible</b>	Prof. Christian Schuster			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Fundamentals of Electrical Engineering			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>				
<i>Knowledge</i>	Students are able to explain the fundamental principles, inter-dependencies, and methods of Electromagnetic Compatibility of electric and electronic systems and to ensure Electromagnetic Compatibility of such systems. They are able to classify and explain the common interference sources and coupling mechanisms. They are capable of explaining the basic principles of shielding and filtering. They are able of giving an overview over measurement and simulation methods for the characterization of Electromagnetic Compatibility in electrical engineering practice.			
<i>Skills</i>	Students are able to apply a series of modeling methods for the Electromagnetic Compatibility of typical electric and electronic systems. They are able to determine the most important effects that these models are predicting in terms of Electromagnetic Compatibility. They can classify these effects and they can quantitatively analyze them. They are capable of deriving problem solving strategies from these predictions and they can adapt them to applications in electrical engineering practice. They can evaluate their problem solving strategies against each other.			
<b>Personal Competence</b>				
<i>Social Competence</i>	Students are able to work together on subject related tasks in small groups. They are able to present their results effectively in English, during laboratory work and exercises, e.g..			
<i>Autonomy</i>	Students are capable to gather necessary information from the references provided and relate that information to the context of the lecture. They are able to make a connection between their knowledge obtained in this lecture with the content of other lectures (e.g. Theoretical Electrical Engineering and Communication Theory). They can communicate problems and solutions in the field of Electromagnetic Compatibility in english language.			
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70			
<b>Credit points</b>	6			
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>	<b>Description</b>
	Yes	None	Presentation	
<b>Examination</b>	Oral exam			
<b>Examination duration and scale</b>	45 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Microwave Engineering, Optics, and Electromagnetic Compatibility: Elective Compulsory Mechatronics: Technical Complementary Course: Elective Compulsory Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory			

Course L0743: EMC I: Coupling Mechanisms, Countermeasures, and Test Procedures	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42
<b>Lecturer</b>	Prof. Christian Schuster
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction to Electromagnetic Compatibility (EMC)</li> <li>• Interference sources in time an frequency domain</li> <li>• Coupling mechanisms</li> <li>• Transmission lines and coupling to electromagnetic fields</li> <li>• Shielding</li> <li>• Filters</li> <li>• EMC test procedures</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>• C.R. Paul: "Introduction to Electromagnetic Compatibility", 2nd ed., (Wiley, New Jersey, 2006).</li> <li>• A.J. Schwab und W. Kürner: "Elektromagnetische Verträglichkeit", 6. Auflage, (Springer, Berlin 2010).</li> <li>• F.M. Tesche, M.V. Ianoz, and T. Karlsson: "EMC Analysis Methods and Computational Models", (Wiley, New York, 1997).</li> </ul>

Course L0744: EMC I: Coupling Mechanisms, Countermeasures, and Test Procedures	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Christian Schuster
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	The exercise sessions serve to deepen the understanding of the concepts of the lecture.
<b>Literature</b>	<ul style="list-style-type: none"> <li>• C.R. Paul: "Introduction to Electromagnetic Compatibility", 2nd ed., (Wiley, New Jersey, 2006).</li> <li>• A.J. Schwab und W. Kürner: "Elektromagnetische Verträglichkeit", 6. Auflage, (Springer, Berlin 2010).</li> <li>• F.M. Tesche, M.V. Ianoz, and T. Karlsson: "EMC Analysis Methods and Computational Models", (Wiley, New York, 1997).</li> <li>• Scientific articles and papers</li> </ul>

Course L0745: EMC I: Coupling Mechanisms, Countermeasures, and Test Procedures	
<b>Typ</b>	Practical Course
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Christian Schuster
<b>Language</b>	DE/EN
<b>Cycle</b>	SoSe
<b>Content</b>	<p>Laboratory experiments serve to practically investigate the following EMC topics:</p> <ul style="list-style-type: none"> <li>• Shielding</li> <li>• Conducted EMC test procedures</li> <li>• The GTEM-cell as an environment for radiated EMC test</li> </ul>
<b>Literature</b>	Versuchsbeschreibungen und zugehörige Literatur werden innerhalb der Veranstaltung bereit gestellt.

Module M0919: Laboratory: Digital Circuit Design				
Courses				
Title	Typ	Hrs/wk	CP	
Laboratory: Digital Circuit Design (L0694)	Project-/problem-based Learning	2	6	
<b>Module Responsible</b>	Prof. Matthias Kuhl			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Basic knowledge of semiconductor devices and circuit design			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b> <i>Knowledge</i>	<ul style="list-style-type: none"> <li>Students can explain the structure and philosophy of the software framework for circuit design.</li> <li>Students can determine all necessary input parameters for circuit simulation.</li> <li>Students are able to explain the functions of the logic gates of their digital design.</li> <li>Students can explain the algorithms of checking routines.</li> <li>Students are able to select the appropriate transistor models for fast and accurate simulations.</li> </ul>			
<i>Skills</i>				
<b>Personal Competence</b> <i>Social Competence</i>				
<i>Autonomy</i>				
<b>Workload in Hours</b>	Independent Study Time 152, Study Time in Lecture 28			
<b>Credit points</b>	6			
<b>Course achievement</b>	None			
<b>Examination</b>	Subject theoretical and practical work			
<b>Examination duration and scale</b>	30 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory			

<b>Course L0694: Laboratory: Digital Circuit Design</b>	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	2
<b>CP</b>	6
<b>Workload in Hours</b>	Independent Study Time 152, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Matthias Kuhl
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Definition of specifications</li> <li>• Architecture studies</li> <li>• Digital simulation flow</li> <li>• Philosophy of standard cells</li> <li>• Placement and routing of standard cells</li> <li>• Layout generation</li> <li>• Design checking routines</li> </ul>
<b>Literature</b>	Handouts will be distributed

Module M0645: Fibre and Integrated Optics				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Fibre and Integrated Optics (L0363)		Lecture	2	3
Fibre and Integrated Optics (Problem Solving Course) (L0365)		Recitation Section (small)	1	1
<b>Module Responsible</b>	Prof. Manfred Eich			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Basic principles of electrodynamics and optics			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>				
<i>Knowledge</i>	Students can explain the fundamental mathematical and physical relations and technological basics of guided optical waves. They can describe integrated optical as well as fibre optical structures. They can give an overview on the applications of integrated optical components in optical signal processing.			
<i>Skills</i>	Students can generate models and derive mathematical descriptions in relation to fibre optical and integrated optical wave propagation. They can derive approximative solutions and judge factors influential on the components' performance.			
<b>Personal Competence</b>				
<i>Social Competence</i>	Students can jointly solve subject related problems in groups. They can present their results effectively within the framework of the problem solving course.			
<i>Autonomy</i>	Students are capable to extract relevant information from the provided references and to relate this information to the content of the lecture. They can reflect their acquired level of expertise with the help of lecture accompanying measures such as exam typical exam questions. Students are able to connect their knowledge with that acquired from other lectures.			
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42			
<b>Credit points</b>	4			
<b>Course achievement</b>	None			
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	60 minutes			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Microwave Engineering, Optics, and Electromagnetic Compatibility: Elective Compulsory Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory			

Course L0363: Fibre and Integrated Optics	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Dr. Hagen Renner
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Theory of optical waveguides</li> <li>• Coupling to and from waveguides</li> <li>• Losses</li> <li>• Linear and nonlinear dispersion</li> <li>• Components and technical applications</li> </ul>
<b>Literature</b>	Bahaa E. A. Saleh, Malvin Carl Teich, Fundamentals of Photonics, Wiley 2007 Hunsperger, R.G., Integrated Optics: Theory and Technology, Springer, 2002 Agrawal, G.P., Fiber-Optic Communication Systems, Wiley, 2002, ISBN 0471215716 Marcuse, D., Theory of Dielectric Optical Waveguides, Academic Press, 1991, ISBN 0124709516 Tamir, T. (ed), Guided-Wave Optoelectronics, Springer, 1990

<b>Course L0365: Fibre and Integrated Optics (Problem Solving Course)</b>	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Dr. Hagen Renner
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	See lecture Fibre and Integrated Optics
<b>Literature</b>	See lecture Fibre and Integrated Optics

Module M0643: Optoelectronics I - Wave Optics				
Courses				
Title	Typ	Hrs/wk	CP	
Optoelectronics I: Wave Optics (L0359)	Lecture	2	3	
Optoelectronics I: Wave Optics (Problem Solving Course) (L0361)	Recitation Section (small)	1	1	
<b>Module Responsible</b>	Dr. Alexander Petrov			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Basics in electrodynamics, calculus			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>				
<i>Knowledge</i>	Students can explain the fundamental mathematical and physical relations of freely propagating optical waves. They can give an overview on wave optical phenomena such as diffraction, reflection and refraction, etc. Students can describe waveoptics based components such as electrooptical modulators in an application oriented way.			
<i>Skills</i>	Students can generate models and derive mathematical descriptions in relation to free optical wave propagation. They can derive approximative solutions and judge factors influential on the components' performance.			
<b>Personal Competence</b>				
<i>Social Competence</i>	Students can jointly solve subject related problems in groups. They can present their results effectively within the framework of the problem solving course.			
<i>Autonomy</i>	Students are capable to extract relevant information from the provided references and to relate this information to the content of the lecture. They can reflect their acquired level of expertise with the help of lecture accompanying measures such as exam typical exam questions. Students are able to connect their knowledge with that acquired from other lectures.			
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42			
<b>Credit points</b>	4			
<b>Course achievement</b>	None			
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	60 minutes			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Electrical Engineering: Specialisation Microwave Engineering, Optics, and Electromagnetic Compatibility: Elective Compulsory Materials Science: Specialisation Nano and Hybrid Materials: Elective Compulsory Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory Renewable Energies: Specialisation Solar Energy Systems: Elective Compulsory			

<b>Course L0359: Optoelectronics I: Wave Optics</b>	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Dr. Alexander Petrov
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Introduction to optics</li> <li>• Electromagnetic theory of light</li> <li>• Interference</li> <li>• Coherence</li> <li>• Diffraction</li> <li>• Fourier optics</li> <li>• Polarisation and Crystal optics</li> <li>• Matrix formalism</li> <li>• Reflection and transmission</li> <li>• Complex refractive index</li> <li>• Dispersion</li> <li>• Modulation and switching of light</li> </ul>
<b>Literature</b>	Bahaa E. A. Saleh, Malvin Carl Teich, Fundamentals of Photonics, Wiley 2007 Hecht, E., Optics, Benjamin Cummings, 2001 Goodman, J.W. Statistical Optics, Wiley, 2000 Lauterborn, W., Kurz, T., Coherent Optics: Fundamentals and Applications, Springer, 2002

<b>Course L0361: Optoelectronics I: Wave Optics (Problem Solving Course)</b>	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Dr. Alexander Petrov
<b>Language</b>	EN
<b>Cycle</b>	SoSe
<b>Content</b>	see lecture Optoelectronics 1 - Wave Optics
<b>Literature</b>	see lecture Optoelectronics 1 - Wave Optics



Module M0781: EMC II: Signal Integrity and Power Supply of Electronic Systems				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
EMC II: Signal Integrity and Power Supply of Electronic Systems (L0770)		Lecture	3	4
EMC II: Signal Integrity and Power Supply of Electronic Systems (L0771)		Recitation Section (small)	1	1
EMC II: Signal Integrity and Power Supply of Electronic Systems (L0774)		Practical Course	1	1
<b>Module Responsible</b>	Prof. Christian Schuster			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Fundamentals of electrical engineering			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>				
<i>Knowledge</i>	Students are able to explain the fundamental principles, inter-dependencies, and methods of signal and power integrity of electronic systems. They are able to relate signal and power integrity to the context of interference-free design of such systems, i.e. their electromagnetic compatibility. They are capable of explaining the basic behavior of signals and power supply in typical packages and interconnects. They are able to propose and describe problem solving strategies for signal and power integrity issues. They are capable of giving an overview over measurement and simulation methods for characterization of signal and power integrity in electrical engineering practice.			
<i>Skills</i>	Students are able to apply a series of modeling methods for characterization of electromagnetic field behavior in packages and interconnect structure of electronic systems. They are able to determine the most important effects that these models are predicting in terms of signal and power integrity. They can classify these effects and they can quantitatively analyze them. They are capable of deriving problem solving strategies from these predictions and they can adapt them to applications in electrical engineering practice. They can evaluate their problem solving strategies against each other.			
<b>Personal Competence</b>				
<i>Social Competence</i>	Students are able to work together on subject related tasks in small groups. They are able to present their results effectively in English (e.g. during CAD exercises).			
<i>Autonomy</i>	Students are capable to gather necessary information from the references provided and relate that information to the context of the lecture. They are able to make a connection between their knowledge obtained in this lecture with the content of other lectures (e.g. theory of electromagnetic fields, communications, and semiconductor circuit design). They can communicate problems and solutions in the field of signal integrity and power supply of interconnect and packages in English.			
<b>Workload in Hours</b>	Independent Study Time 110, Study Time in Lecture 70			
<b>Credit points</b>	6			
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>	<b>Description</b>
	Yes	None	Presentation	
<b>Examination</b>	Oral exam			
<b>Examination duration and scale</b>	45 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Microwave Engineering, Optics, and Electromagnetic Compatibility: Elective Compulsory Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Mechatronics: Technical Complementary Course: Elective Compulsory Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory			

<b>Course L0770: EMC II: Signal Integrity and Power Supply of Electronic Systems</b>	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42
<b>Lecturer</b>	Prof. Christian Schuster
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>- The role of packages and interconnects in electronic systems</li> <li>- Components of packages and interconnects in electronic systems</li> <li>- Main goals and concepts of signal and power integrity of electronic systems</li> <li>- Repeat of relevant concepts from the theory electromagnetic fields</li> <li>- Properties of digital signals and systems</li> <li>- Design and characterization of signal integrity</li> <li>- Design and characterization of power supply</li> <li>- Techniques and devices for measurements in time- and frequency-domain</li> <li>- CAD tools for electrical analysis and design of packages and interconnects</li> <li>- Connection to overall electromagnetic compatibility of electronic systems</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>- J. Franz, "EMV: Störungssicherer Aufbau elektronischer Schaltungen", Springer (2012)</li> <li>- R. Tummala, "Fundamentals of Microsystems Packaging", McGraw-Hill (2001)</li> <li>- S. Ramo, J. Whinnery, T. Van Duzer, "Fields and Waves in Communication Electronics", Wiley (1994)</li> <li>- S. Thierauf, "Understanding Signal Integrity", Artech House (2010)</li> <li>- M. Swaminathan, A. Engin, "Power Integrity Modeling and Design for Semiconductors and Systems", Prentice-Hall (2007)</li> </ul>

<b>Course L0771: EMC II: Signal Integrity and Power Supply of Electronic Systems</b>	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Christian Schuster
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

<b>Course L0774: EMC II: Signal Integrity and Power Supply of Electronic Systems</b>	
<b>Typ</b>	Practical Course
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Prof. Christian Schuster
<b>Language</b>	DE/EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>- The role of packages and interconnects in electronic systems</li> <li>- Components of packages and interconnects in electronic systems</li> <li>- Main goals and concepts of signal and power integrity of electronic systems</li> <li>- Repeat of relevant concepts from the theory electromagnetic fields</li> <li>- Properties of digital signals and systems</li> <li>- Design and characterization of signal integrity</li> <li>- Design and characterization of power supply</li> <li>- Techniques and devices for measurements in time- and frequency-domain</li> <li>- CAD tools for electrical analysis and design of packages and interconnects</li> <li>- Connection to overall electromagnetic compatibility of electronic systems</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>- J. Franz, "EMV: Störungssicherer Aufbau elektronischer Schaltungen", Springer (2012)</li> <li>- R. Tummala, "Fundamentals of Microsystems Packaging", McGraw-Hill (2001)</li> <li>- S. Ramo, J. Whinnery, T. Van Duzer, "Fields and Waves in Communication Electronics", Wiley (1994)</li> <li>- S. Thierauf, "Understanding Signal Integrity", Artech House (2010)</li> <li>- M. Swaminathan, A. Engin, "Power Integrity Modeling and Design for Semiconductors and Systems", Prentice-Hall (2007)</li> </ul>

Module M0913: Mixed-signal Circuit Design				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Mixed-signal Circuit Design (L0764)		Lecture	2	3
Mixed-signal Circuit Design (L1063)		Project-/problem-based Learning	2	3
<b>Module Responsible</b>	Prof. Matthias Kuhl			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Advanced knowledge of analog or digital MOS devices and circuits			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>	<ul style="list-style-type: none"> <li>Students can explain the descriptive parameters of mixed-signal systems</li> <li>Students can explain various architectures of analog-to-digital and digital-to-analog converters</li> <li>Students are able to explain the fundamental limitations of different analog-to-digital and digital-to-analog converters</li> </ul>			
<i>Knowledge</i>				
<i>Skills</i>				
<b>Personal Competence</b>				
<i>Social Competence</i>	<ul style="list-style-type: none"> <li>Students can team up with one or several partners who may have different professional backgrounds</li> <li>Students are able to work by their own or in small groups for solving problems and answer scientific questions.</li> </ul>			
<i>Autonomy</i>	<ul style="list-style-type: none"> <li>Students are able to assess their knowledge in a realistic manner.</li> <li>Students are able to draw scenarios for estimation of the impact of an increase of data vs. an increase of energy on the future lifestyle of the society.</li> </ul>			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56			
<b>Credit points</b>	6			
<b>Course achievement</b>	<b>Compulsory</b>	<b>Bonus</b>	<b>Form</b>	<b>Description</b>
	Yes	5 %	Subject theoretical and practical work	
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	90 min			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory			

Course L0764: Mixed-signal Circuit Design	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Matthias Kuhl
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>Differences between analog and digital filtering of electrical signals</li> <li>Quantization error and its consideration in electrical circuits</li> <li>Architectures of state-of-the-art digital-to-analog converters</li> <li>Architectures of state-of-the-art analog-to-digital converters</li> <li>Differentiation between Nyquist and oversampling converters</li> <li>noise in ADCs and DACs</li> </ul>
<b>Literature</b>	<ul style="list-style-type: none"> <li>R. J. Baker, „CMOS-Circuit Design, Layout, and Simulation“, Wiley &amp; Sons, IEEE Press, 2010</li> <li>B. Razavi, "Design of Analog CMOS Integrated Circuits", McGraw-Hill Education Ltd, 2000</li> </ul>

<b>Course L1063: Mixed-signal Circuit Design</b>	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Matthias Kuhl
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1688: Selected Aspects of Microelectronics and Microsystems			
<b>Courses</b>			
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b> <b>CP</b>
Selected Aspects of Microelectronics and Microsystems (L2678)		Lecture	3                  4
Selected Aspects of Microelectronics and Microsystems (L2679)		Recitation Section (small)	1                  2
<b>Module Responsible</b>	Prof. Hoc Khiem Trieu		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b> <i>Knowledge</i> <i>Skills</i>			
<b>Personal Competence</b> <i>Social Competence</i> <i>Autonomy</i>			
<b>Workload in Hours</b>	Independent Study Time 124, Study Time in Lecture 56		
<b>Credit points</b>	6		
<b>Course achievement</b>	None		
<b>Examination</b>	Oral exam		
<b>Examination duration and scale</b>	30 min		
<b>Assignment for the Following Curricula</b>	Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory		

Course L2678: Selected Aspects of Microelectronics and Microsystems	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	3
<b>CP</b>	4
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42
<b>Lecturer</b>	Dozenten des SD E
<b>Language</b>	EN
<b>Cycle</b>	WiSe/SoSe
<b>Content</b>	
<b>Literature</b>	

Course L2679: Selected Aspects of Microelectronics and Microsystems	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	2
<b>Workload in Hours</b>	Independent Study Time 46, Study Time in Lecture 14
<b>Lecturer</b>	Dozenten des SD E
<b>Language</b>	EN
<b>Cycle</b>	WiSe/SoSe
<b>Content</b>	See interlocking course
<b>Literature</b>	See interlocking course

Module M1589: Laboratory: Analog Circuit Design			
Courses			
Title	Typ	Hrs/wk	CP
Laboratory: Analog Circuit Design (L0692)	Project-/problem-based Learning	2	6
<b>Module Responsible</b>	Prof. Matthias Kuhl		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>	Basic knowledge of semiconductor devices and circuit design		
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b> <i>Knowledge</i>	<ul style="list-style-type: none"> <li>• Students can explain the structure and philosophy of the software framework for circuit design.</li> <li>• Students can determine all necessary input parameters for circuit simulation.</li> <li>• Students know the basics physics of the analog behavior.</li> <li>• Students can explain the algorithms of circuit verification.</li> <li>• Students are able to select the appropriate transistor models for fast and accurate simulations.</li> </ul>		
<i>Skills</i>			
<b>Personal Competence</b> <i>Social Competence</i>			
<i>Autonomy</i>	<ul style="list-style-type: none"> <li>• Students can activate and execute all necessary checking routines for verification of proper circuit functionality.</li> <li>• Students can define the specifications of the electronic circuits to be designed.</li> <li>• Students can optimize the electronic circuits for low-noise and low-power.</li> <li>• Students can develop analog circuits for specific applications.</li> </ul>		
<b>Workload in Hours</b>	Independent Study Time 152, Study Time in Lecture 28		
<b>Credit points</b>	6		
<b>Course achievement</b>	None		
<b>Examination</b>	Subject theoretical and practical work		
<b>Examination duration and scale</b>	30 min		
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory		

<b>Course L0692: Laboratory: Analog Circuit Design</b>	
<b>Typ</b>	Project-/problem-based Learning
<b>Hrs/wk</b>	2
<b>CP</b>	6
<b>Workload in Hours</b>	Independent Study Time 152, Study Time in Lecture 28
<b>Lecturer</b>	Prof. Matthias Kuhl, Weitere Mitarbeiter
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Input desk for circuits</li> <li>• Algorithms for simulation</li> <li>• MOS transistor model</li> <li>• Simulation of analog circuits</li> <li>• Placement and routing</li> <li>• Generation of layouts</li> <li>• Design checking routines</li> <li>• Postlayout simulations</li> </ul>
<b>Literature</b>	Handouts to be distributed



Module M0644: Optoelectronics II - Quantum Optics				
<b>Courses</b>				
<b>Title</b>		<b>Typ</b>	<b>Hrs/wk</b>	<b>CP</b>
Optoelectronics II: Quantum Optics (L0360)		Lecture	2	3
Optoelectronics II: Quantum Optics (Problem Solving Course) (L0362)		Recitation Section (small)	1	1
<b>Module Responsible</b>	Dr. Alexander Petrov			
<b>Admission Requirements</b>	None			
<b>Recommended Previous Knowledge</b>	Basic principles of electrodynamics, optics and quantum mechanics			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results			
<b>Professional Competence</b>				
<i>Knowledge</i>	Students can explain the fundamental mathematical and physical relations of quantum optical phenomena such as absorption, stimulated and spontaneous emission. They can describe material properties as well as technical solutions. They can give an overview on quantum optical components in technical applications.			
<i>Skills</i>	Students can generate models and derive mathematical descriptions in relation to quantum optical phenomena and processes. They can derive approximative solutions and judge factors influential on the components' performance.			
<b>Personal Competence</b>				
<i>Social Competence</i>	Students can jointly solve subject related problems in groups. They can present their results effectively within the framework of the problem solving course.			
<i>Autonomy</i>	Students are capable to extract relevant information from the provided references and to relate this information to the content of the lecture. They can reflect their acquired level of expertise with the help of lecture accompanying measures such as exam typical exam questions. Students are able to connect their knowledge with that acquired from other lectures.			
<b>Workload in Hours</b>	Independent Study Time 78, Study Time in Lecture 42			
<b>Credit points</b>	4			
<b>Course achievement</b>	None			
<b>Examination</b>	Written exam			
<b>Examination duration and scale</b>	60 minutes			
<b>Assignment for the Following Curricula</b>	Electrical Engineering: Specialisation Nanoelectronics and Microsystems Technology: Elective Compulsory Electrical Engineering: Specialisation Microwave Engineering, Optics, and Electromagnetic Compatibility: Elective Compulsory Materials Science: Specialisation Nano and Hybrid Materials: Elective Compulsory Microelectronics and Microsystems: Specialisation Microelectronics Complements: Elective Compulsory			

Course L0360: Optoelectronics II: Quantum Optics	
<b>Typ</b>	Lecture
<b>Hrs/wk</b>	2
<b>CP</b>	3
<b>Workload in Hours</b>	Independent Study Time 62, Study Time in Lecture 28
<b>Lecturer</b>	Dr. Alexander Petrov
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	<ul style="list-style-type: none"> <li>• Generation of light</li> <li>• Photons</li> <li>• Thermal and nonthermal light</li> <li>• Laser amplifier</li> <li>• Noise</li> <li>• Optical resonators</li> <li>• Spectral properties of laser light</li> <li>• CW-lasers (gas, solid state, semiconductor)</li> <li>• Pulsed lasers</li> </ul>
<b>Literature</b>	Bahaa E. A. Saleh, Malvin Carl Teich, Fundamentals of Photonics, Wiley 2007 Demtröder, W., Laser Spectroscopy: Basic Concepts and Instrumentation, Springer, 2002 Kasap, S.O., Optoelectronics and Photonics: Principles and Practices, Prentice Hall, 2001 Yariv, A., Quantum Electronics, Wiley, 1988 Wilson, J., Hawkes, J., Optoelectronics: An Introduction, Prentice Hall, 1997, ISBN: 013103961X Siegman, A.E., Lasers, University Science Books, 1986

<b>Course L0362: Optoelectronics II: Quantum Optics (Problem Solving Course)</b>	
<b>Typ</b>	Recitation Section (small)
<b>Hrs/wk</b>	1
<b>CP</b>	1
<b>Workload in Hours</b>	Independent Study Time 16, Study Time in Lecture 14
<b>Lecturer</b>	Dr. Alexander Petrov
<b>Language</b>	EN
<b>Cycle</b>	WiSe
<b>Content</b>	see lecture Optoelectronics 1 - Wave Optics
<b>Literature</b>	see lecture Optoelectronics 1 - Wave Optics

**Thesis**

**Module M1801: Master thesis (dual study program)**

**Courses**

Title	Typ	Hrs/wk	CP
<b>Module Responsible</b>	Professoren der TUHH		
<b>Admission Requirements</b>	None		
<b>Recommended Previous Knowledge</b>			
<b>Educational Objectives</b>	After taking part successfully, students have reached the following learning results		
<b>Professional Competence</b>			
<i>Knowledge</i>	Dual students ... <ul style="list-style-type: none"> <li>... use the specialised knowledge (facts, theories and methods) from their field of study and the acquired professional knowledge confidently to deal with technical and practical professional issues.</li> <li>... can explain the relevant approaches and terminologies in depth in one or more of their subject's specialist areas, describe current developments and take a critical stance.</li> <li>... formulate their own research assignment to tackle a professional problem and contextualise it within their subject area. They ascertain the current state of research and critically assess it.</li> </ul>		
<i>Skills</i>	Dual students ... <ul style="list-style-type: none"> <li>... can select suitable methods for the respective subject-related professional problem, apply them and develop them further as required.</li> <li>... assess knowledge and methods acquired during their studies (including practical phases) and apply their expertise to complex and/or incompletely defined problems in a solution- and application-oriented manner.</li> <li>... acquire new academic knowledge in their subject area and critically evaluate it.</li> </ul>		
<b>Personal Competence</b>			
<i>Social Competence</i>	Dual students ... <ul style="list-style-type: none"> <li>... can present a professional problem in the form of an academic question in a structured, comprehensible and factually correct manner, both in writing and orally, for a specialist audience and for professional stakeholders.</li> <li>... answer questions as part of a professional discussion in an expert, appropriate manner. They represent their own points of view and assessments convincingly.</li> </ul>		
<i>Autonomy</i>	Dual students ... <ul style="list-style-type: none"> <li>... can structure their own project into work packages, work through them at an academic level and reflect on them with regard to feasible courses of action for professional practice.</li> <li>... work in-depth in a partially unknown area within the discipline and acquire the information required to do so.</li> <li>... apply the techniques of academic work comprehensively in their own research work when dealing with an operational problem and question.</li> </ul>		
<b>Workload in Hours</b>	Independent Study Time 900, Study Time in Lecture 0		
<b>Credit points</b>	30		
<b>Course achievement</b>	None		
<b>Examination</b>	Thesis		
<b>Examination duration and scale</b>	According to General Regulations		
<b>Assignment for the Following Curricula</b>	Civil Engineering: Thesis: Compulsory Bioprocess Engineering: Thesis: Compulsory Chemical and Bioprocess Engineering: Thesis: Compulsory Computer Science: Thesis: Compulsory Electrical Engineering: Thesis: Compulsory Energy Systems: Thesis: Compulsory Environmental Engineering: Thesis: Compulsory Aircraft Systems Engineering: Thesis: Compulsory Computer Science in Engineering: Thesis: Compulsory Information and Communication Systems: Thesis: Compulsory International Management and Engineering: Thesis: Compulsory Logistics, Infrastructure and Mobility: Thesis: Compulsory Materials Science: Thesis: Compulsory Mechanical Engineering and Management: Thesis: Compulsory Mechatronics: Thesis: Compulsory Biomedical Engineering: Thesis: Compulsory Microelectronics and Microsystems: Thesis: Compulsory Product Development, Materials and Production: Thesis: Compulsory Renewable Energies: Thesis: Compulsory Naval Architecture and Ocean Engineering: Thesis: Compulsory		

Theoretical Mechanical Engineering: Thesis: Compulsory
Process Engineering: Thesis: Compulsory
Water and Environmental Engineering: Thesis: Compulsory